

## 2-Line Ultra Low Capacitance TVS Diode Array

**Features**

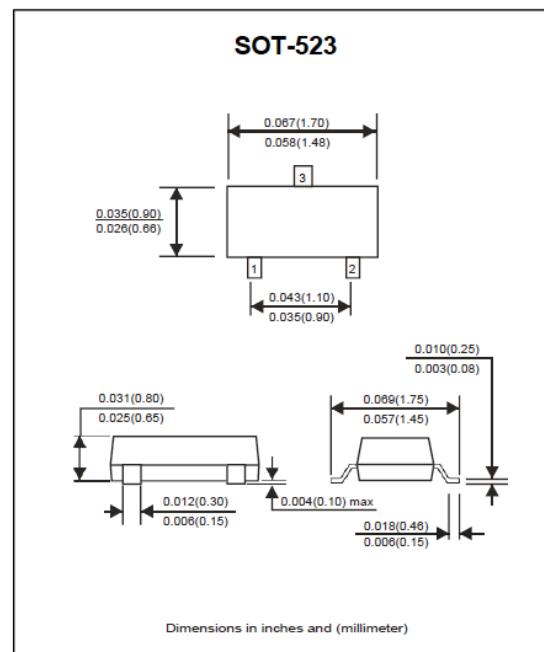
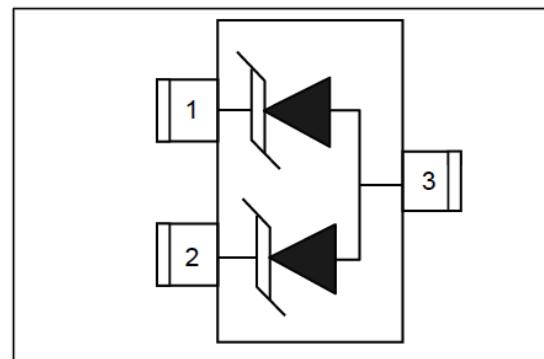
- IEC 61000-4-2 (ESD)  $\pm 25\text{kV}$  (air),  $\pm 20\text{kV}$  (contact)
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 5A (8/20 $\mu\text{s}$ )
- Ultra Low Capacitance
- Low leakage current
- Low clamping voltage
- Operating voltage: 5V
- Protects one bidirectional line or two unidirectional lines

**Applications**

- Mobile Display Digital Interface (MDDI)
- HBT Power Amplifier Protection
- InfiniBand Transceiver Protection
- Photodetector Protection
- Industrial Controls
- USB 2.0

**Mechanical Characteristics**

- Package: SOT-523
- Lead Finish: Matte Tin
- Case Material: "Green" Molding Compound.
- Moisture Sensitivity: Level 3 per J-STD-020
- Material: RoHS compliant

**Circuit and Pin Schematic****Absolute Maximum Ratings ( $T_A = 25^\circ\text{C}$  Unless otherwise noted)**

Parameter	Symbol	Value	Unit
Peak Pulse Power ( $tp = 8/20\mu\text{s}$ )	$P_{PP}$	80	W
Peak Pulse Current ( $tp = 8/20\mu\text{s}$ )	$I_{PP}$	5	A
ESD per IEC 61000-4-2 (Air)	$V_{ESD}$	$\pm 25$	KV
ESD per IEC 61000-4-2 (Contact)		$\pm 20$	KV
Operating Temperature Range	$T_J$	-55 to + 125	°C
Storage Temperature Range	$T_{STG}$	-55 to + 150	°C

Electrical Parameters ( $T_A = 25^\circ\text{C}$  Unless otherwise noted)

Symbol	Parameter	
$I_{PP}$	Reverse Peak Pulse Current	
$V_C$	Clamping Voltage @ $I_{PP}$	
$V_{RWM}$	Reverse Stand-Off Voltage	
$I_R$	Reverse Leakage Current @ $V_{RWM}$	
$V_{BR}$	Breakdown Voltage @ $I_T$	
$I_T$	Test Current	
$I_F$	Forward Current	
$V_F$	Forward Voltage @ $I_F$	

Electrical Characteristics ( $T_A = 25^\circ\text{C}$  Unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Reverse Stand-Off Voltage	$V_{RWM}$	Pin 1 or Pin 2 to Pin 3 and Between Pin1 and Pin2			5.0	V
Reverse breakdown Voltage	$V_{BR}$	$I_T = 1\text{mA}$ , (Pin 1 or Pin 2 to Pin 3)	6.0			V
Reverse leakage current	$I_R$	$V_{RWM} = 5\text{V}$ , (Pin 1 or Pin 2 to Pin 3 and Between Pin 1 and Pin 2)			0.5	$\mu\text{A}$
Clamping Voltage	$V_C$	$I_{PP} = 1\text{A}$ ( $t_p = 8/20\mu\text{s}$ ), (Pin 1 or Pin 2 to Pin 3)			9	V
Clamping Voltage	$V_C$	$I_{PP} = 5\text{A}$ ( $t_p = 8/20\mu\text{s}$ ), (Pin 1 or Pin 2 to Pin 3)			16	V
Junction capacitance	$C_J$	$V_R = 0\text{V}$ , $f = 1\text{MHz}$ , (Pin 1 to Pin 2)		0.3		pF
Junction capacitance	$C_J$	$V_R = 0\text{V}$ , $f = 1\text{MHz}$ , (Pin 1 or Pin 2 to Pin 3)		0.6	0.8	pF

### Typical Performance Characteristics ( $T_A = 25^\circ\text{C}$ Unless otherwise noted)

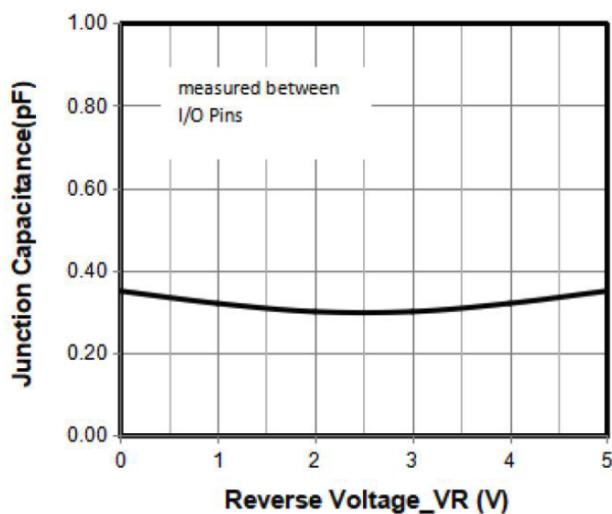


Fig 1. Junction Capacitance vs. Reverse Voltage

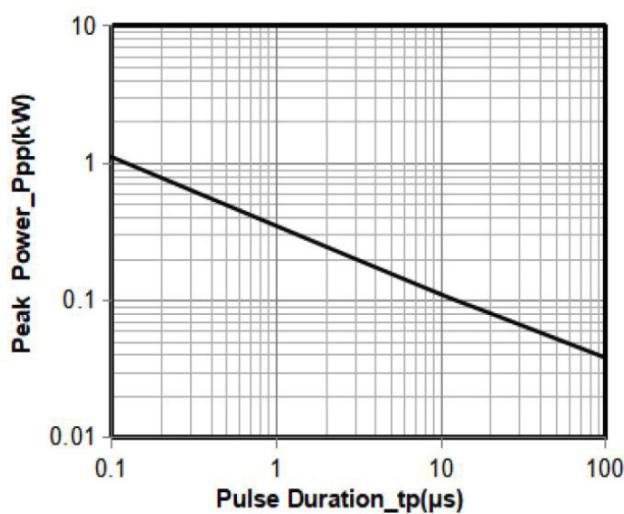


Fig 2. Peak Pulse Power vs. Pulse Time

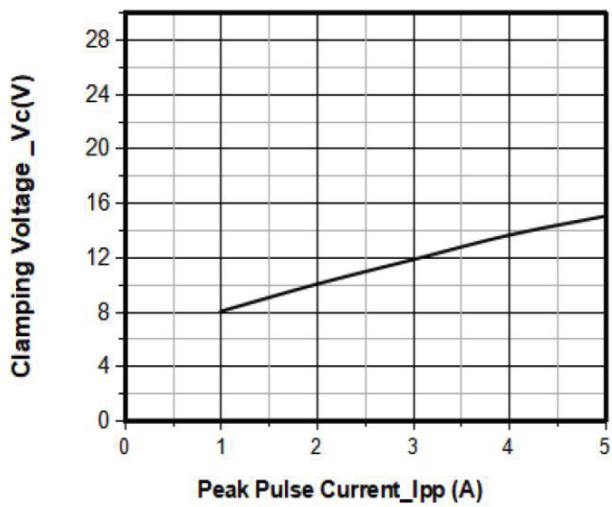


Fig 3. Clamping Voltage vs. Peak Pulse Current

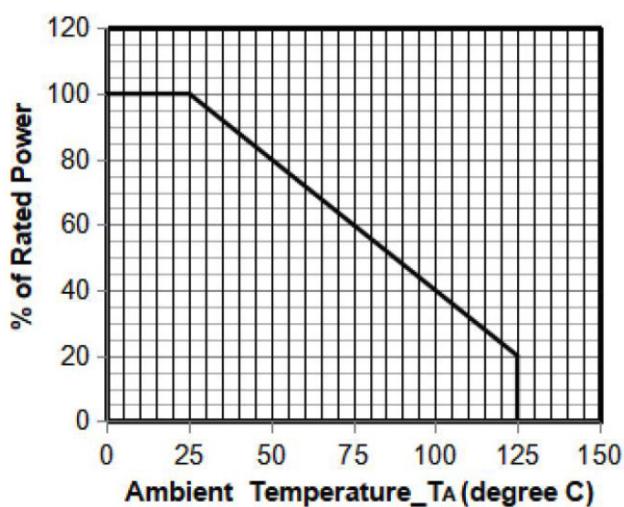


Fig 4. Power Derating Curve

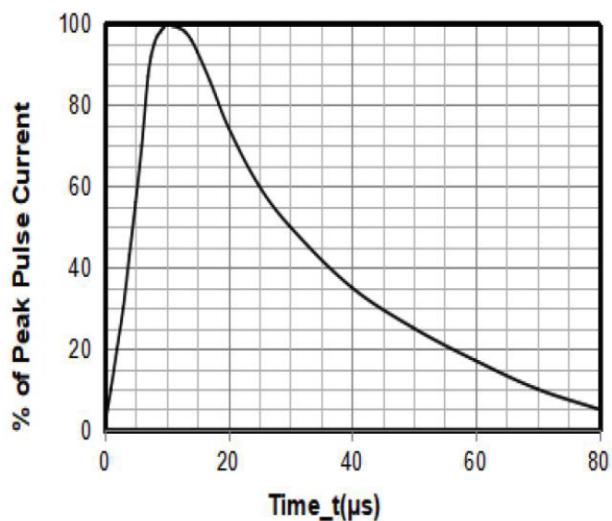
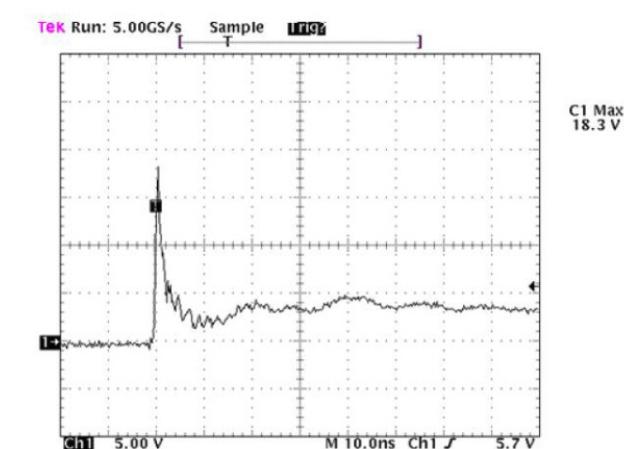


Fig 5. 8 X 20μs Pulse Waveform

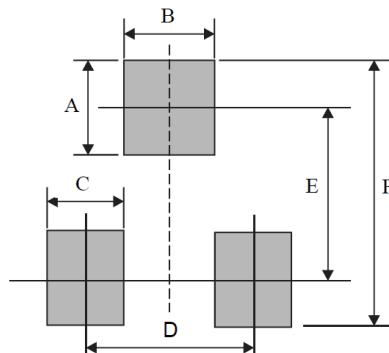


Note: Data is taken with a 10x attenuator

Fig 6. ESD Clamping Voltage  
8 KV Contact per IEC61000-4-2

### Suggested PAD Layout

Symbol	SOT-523	
	(mm)	(inch)
A	0.60	0.024
B	0.50	0.020
C	0.40	0.016
D	1.00	0.039
E	1.24	0.049
F	1.84	0.072



### Marking Code

Part Number	Marking Code	
SC05L2UTP	5B	

### Ordering information

Part Number	Package	Base qty	Reel Size	Delivery mode
		(pcs)	(inch)	
SC05L2UTP	SOT-523	3,000	7	Tape and reel