

N-Channel Enhancement MOSFET

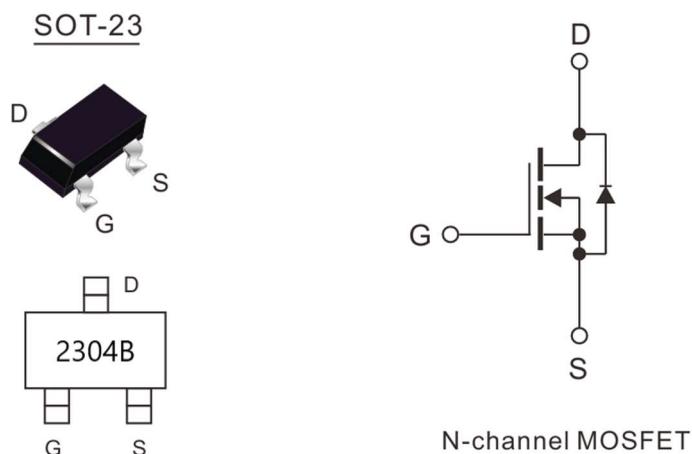
Features

- Trench Power MV MOSFET technology
- High Power and current handing capability
- Voltage Controlled Small Signal Switch
- Halogen-Free & Lead-Free

Product Summary		
V _{DS}	R _{D(on)} (mΩ) Typ	I _D (A)
30V	46@ 4.5V 3A	4.0
	28@ 10V 4A	

Application

- Load Switch for Portable Devices
- Solid-state relays
- DC/DC Converter

Marking information**Absolute Maximum Ratings (at T_A = 25°C unless otherwise noted)**

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous drain current (T _A =25 °C)	I _D	4.0	A
Continuous drain current (T _A =70 °C)	I _D	2.5	A
Peak Drain Current, Pulsed ¹⁾	I _{DM}	20	A
Power Dissipation	P _D	1.0	W
Operating Junction	T _J	-55~150	°C
Storage Temperature Range	T _{stg}	-55~150	°C

Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Ambient ²⁾	R _{θJA}	125	°C/W

Note:

1. Pulse Test: Pulse Width≤300us, Duty cycle ≤2%.
2. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.

Characteristics at $T_J = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit
STATIC PARAMETERS					
Drain-Source Breakdown Voltage at $V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	BV_{DSS}	30			V
Drain-Source Leakage Current at $V_{DS}=30\text{V}$, $V_{GS}=0\text{V}$	I_{DSS}			1	μA
Gate Leakage Current at $V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$	I_{GSS}			± 0.1	μA
Gate-Source Threshold Voltage at $V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$	$V_{GS(\text{th})}$	1.0	1.6	2.2	V
Drain-Source On-State Resistance at $V_{GS}=10\text{V}$, $I_D=4\text{A}$ at $V_{GS}=4.5\text{V}$, $I_D=3\text{A}$	$R_{DS(\text{on})}$		28 46	36 60	$\text{m}\Omega$
DYNAMIC PARAMETERS					
Input Capacitance at $V_{DS}=15\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	C_{iss}		280		pF
Output Capacitance at $V_{DS}=15\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	C_{oss}		45		pF
Reverse Transfer Capacitance at $V_{DS}=15\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	C_{rss}		35		pF
Gate charge total at $V_{DS}=15\text{V}$, $V_{GS}=10\text{V}$, $I_D=4\text{A}$	Q_g		6.5		nC
Gate to Source Charge at $V_{DS}=15\text{V}$, $V_{GS}=10\text{V}$, $I_D=4\text{A}$	Q_{gs}		1.0		nC
Gate to Drain Charge at $V_{DS}=15\text{V}$, $V_{GS}=10\text{V}$, $I_D=4\text{A}$	Q_{gd}		2.0		nC
Reverse Recovery Time at $I_F=4\text{A}$, $dI/dt=300\text{A}/\mu\text{s}$	t_{rr}		6.5		nS
Reverse Recovery Charge at $I_F=4\text{A}$, $dI/dt=300\text{A}/\mu\text{s}$	Q_{rr}		5.0		nC
Turn-On Delay Time at $V_{DD}=15\text{V}$, $I_D=4\text{A}$, $R_{GEN}=2.2\Omega$, $V_{GS}=10\text{V}$	$t_{d(on)}$		3		nS
Turn-On Rise Time at $V_{DD}=15\text{V}$, $I_D=4\text{A}$, $R_{GEN}=2.2\Omega$, $V_{GS}=10\text{V}$	t_r		15		ns
Turn-Off Delay Time at $V_{DD}=15\text{V}$, $I_D=4\text{A}$, $R_{GEN}=2.2\Omega$, $V_{GS}=10\text{V}$	$t_{d(off)}$		12		nS
Turn-Off Fall Time at $V_{DD}=15\text{V}$, $I_D=4\text{A}$, $R_{GEN}=2.2\Omega$, $V_{GS}=10\text{V}$	t_f		3		ns
Body-Diode PARAMETERS					
Drain-Source Diode Forward Voltage at $I_S=3.6\text{A}$, $V_{GS}=0\text{V}$	V_{SD}			1.2	V
Maximum Body-Diode Continuous Current	I_S			4.0	A

Electrical Characteristics Curves

Figure 1. Output Characteristics

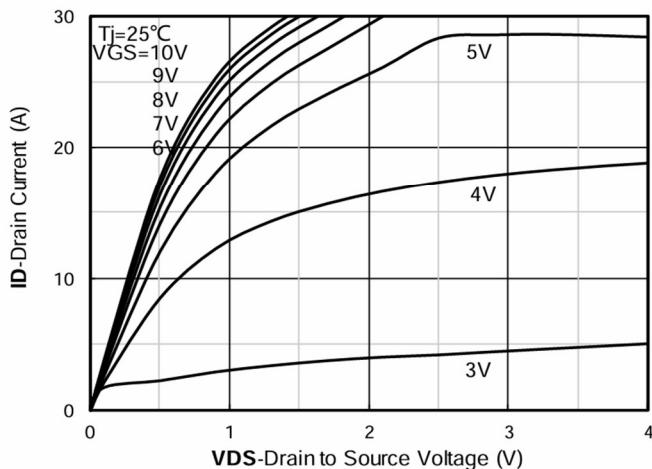


Figure 3. Capacitance Characteristics

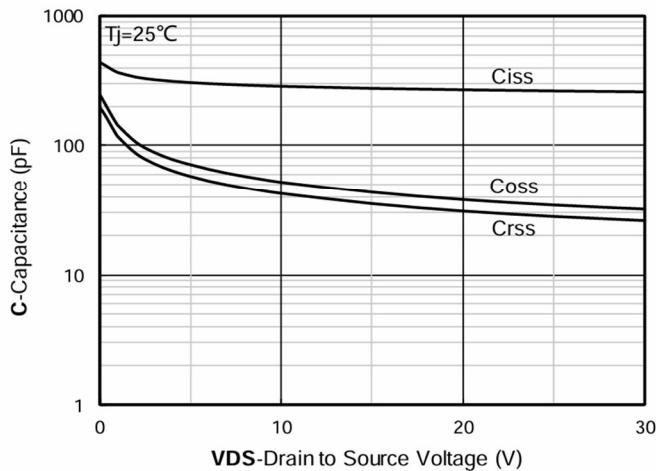


Figure 5. On-Resistance vs Gate to Source Voltage

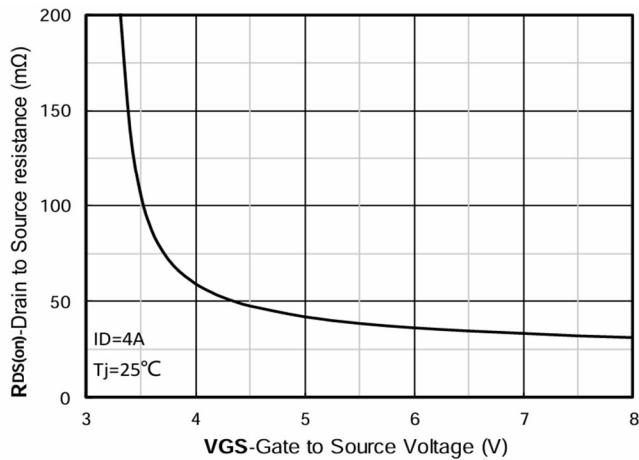


Figure 2. Transfer Characteristics

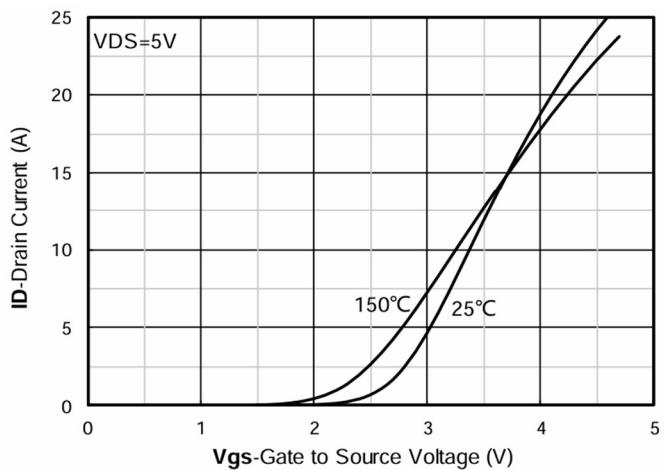


Figure 4. Gate Charge

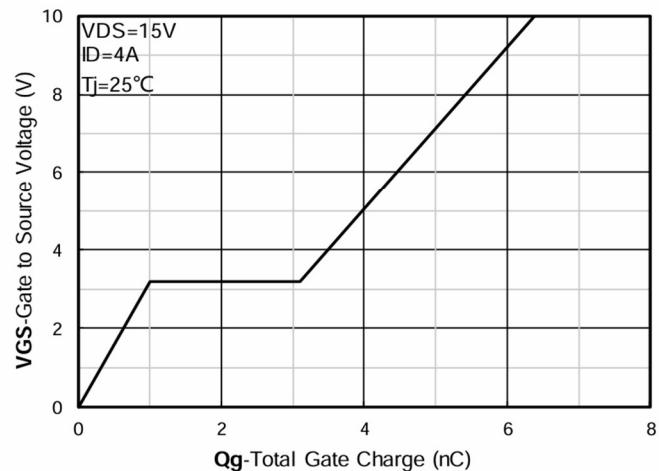
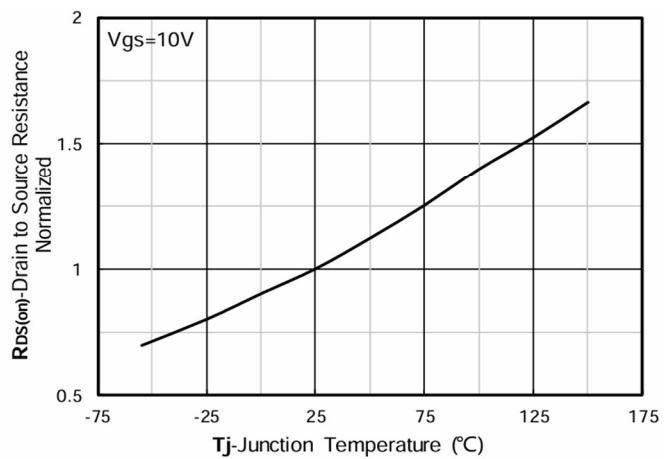


Figure 6. Normalized On-Resistance



Electrical Characteristics Curves

Figure 7. RDS(on) VS Drain Current

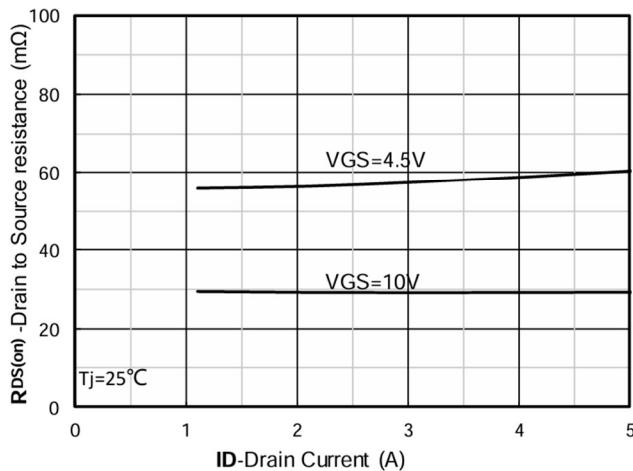


Figure 9. Normalized breakdown voltage

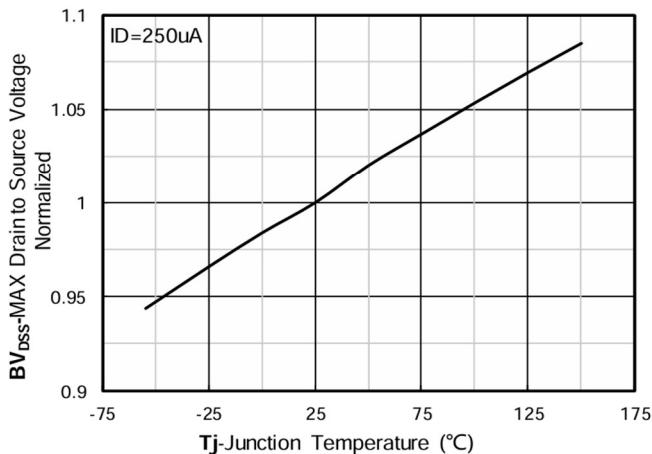


Figure 11. Current dissipation

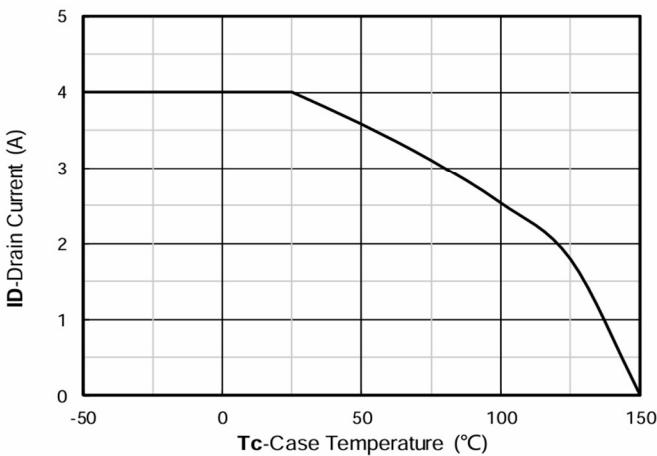


Figure 8. Forward characteristics of reverse diode

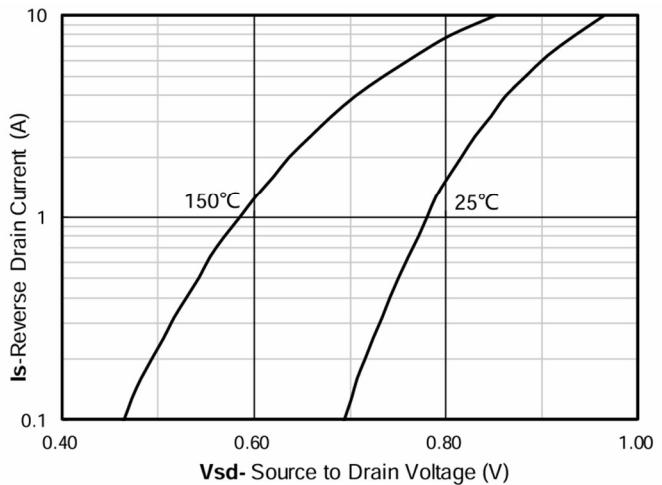


Figure 10. Normalized Threshold voltage

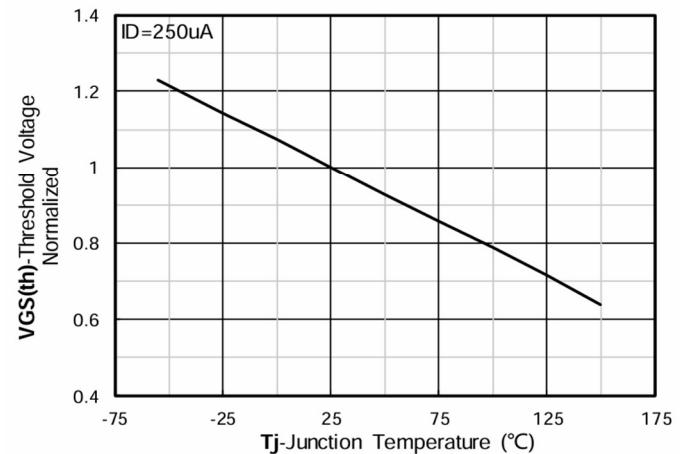
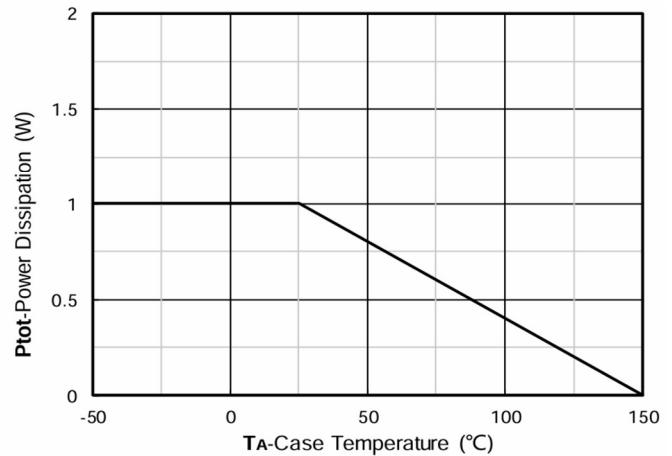


Figure 12. Power dissipation



Electrical Characteristics Curves

Figure 13. Maximum Transient Thermal Impedance

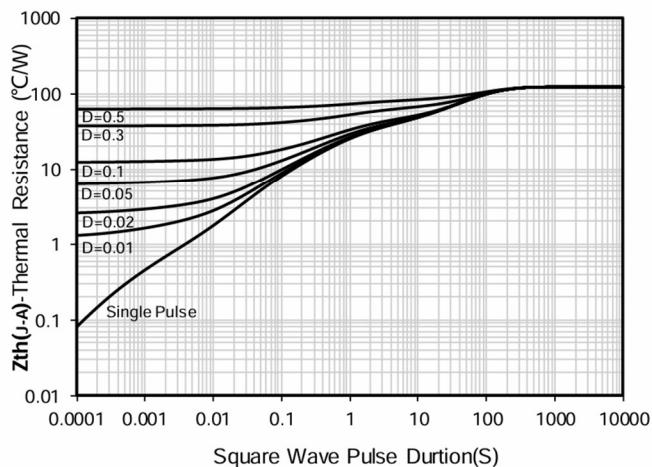
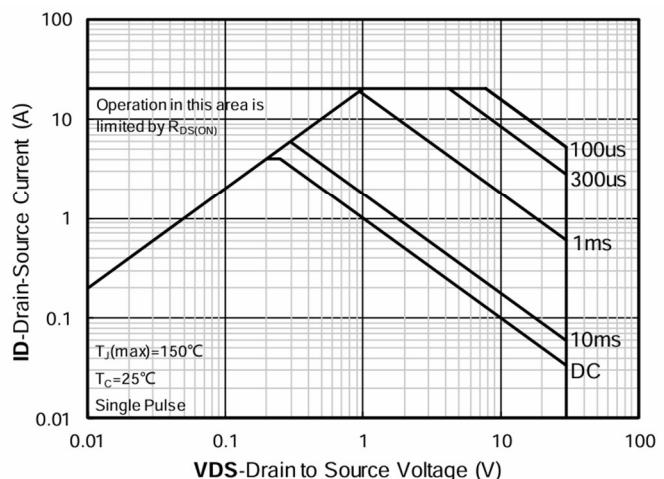
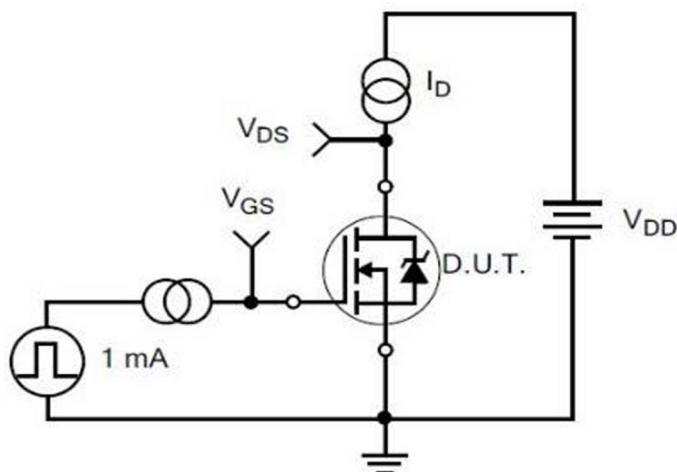


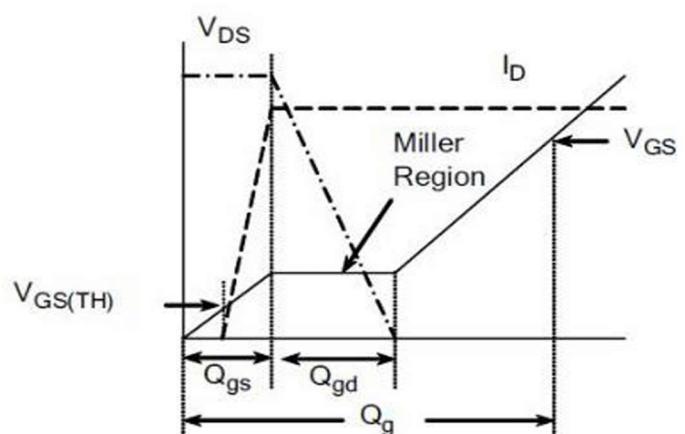
Figure 14. Safe Operation Area



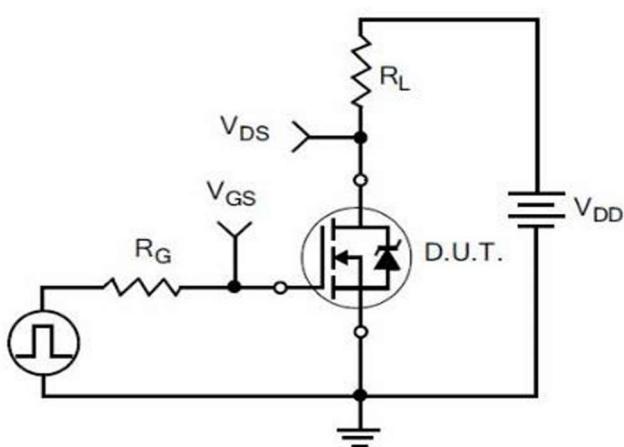
Typical Test Circuit



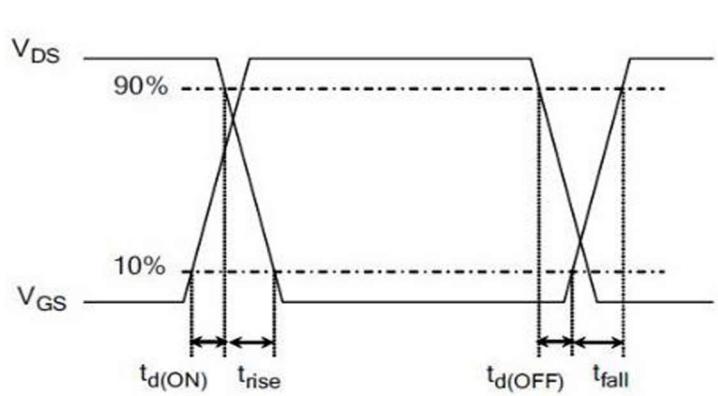
1) Gate Charge Test Circuit



2) . Gate Charge Waveform

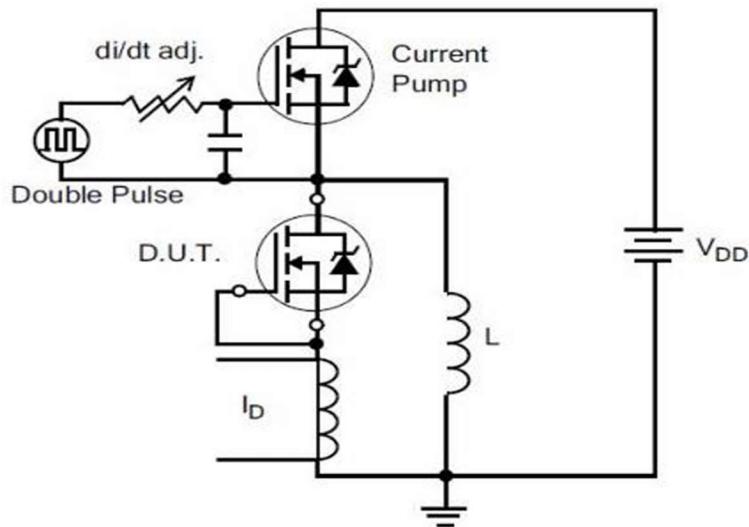


3) Resistive Switching Test Circuit

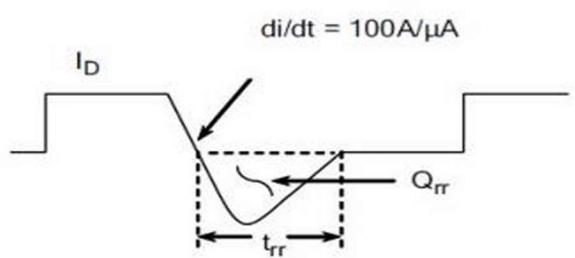


4) Resistive Switching Waveforms

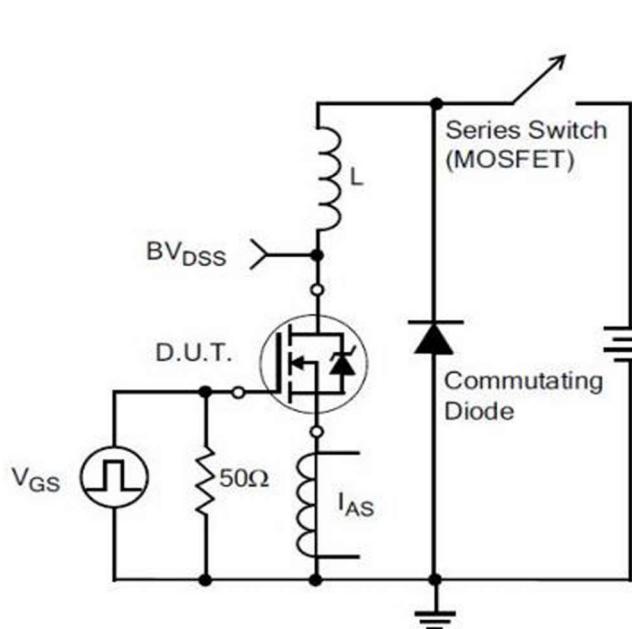
Typical Test Circuit



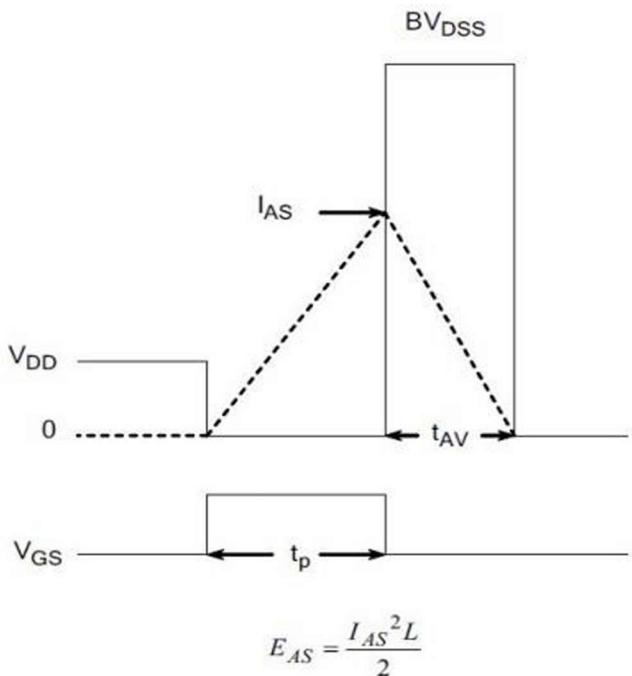
5) Diode Reverse Recovery Test Circuit



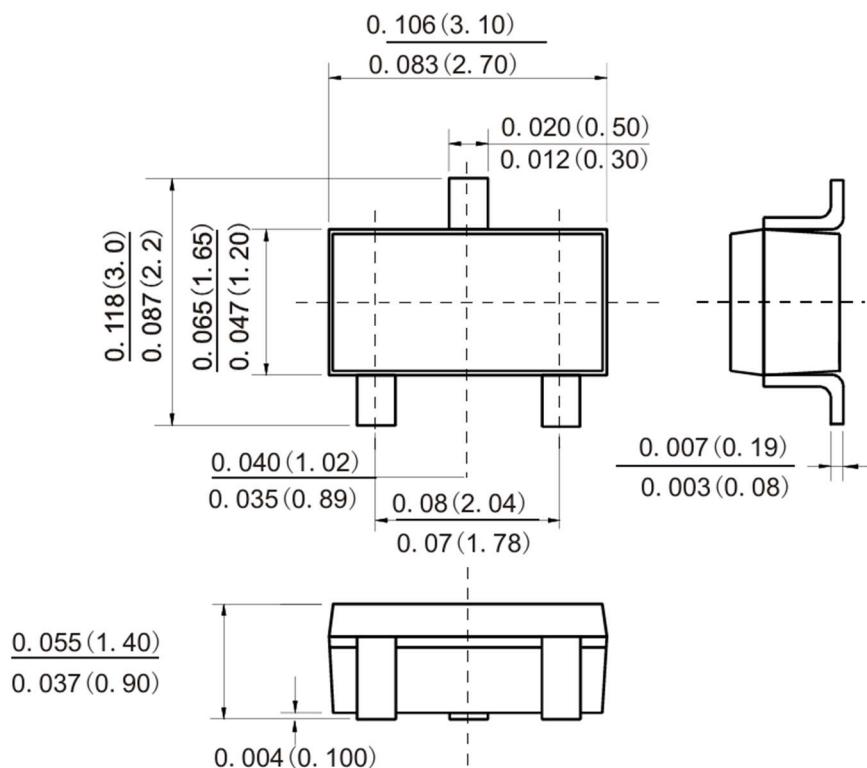
6) Diode Reverse Recovery Waveform



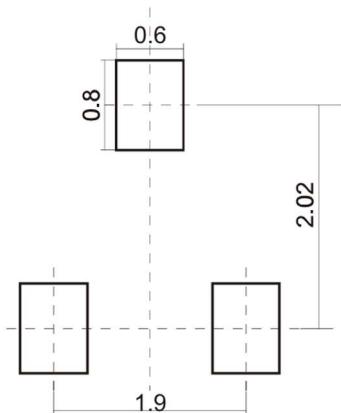
7) Unclamped Inductive Switching Test Circuit



8) Unclamped Inductive Switching Waveforms

Package Outline Dimensions (Units: mm) SOT-23

Dimensions in inches and (millimetres)

Suggested Pad Layout

Dimensions in millimetres

Order Information

Part Number	Package	Quantity
Sh2304B	SOT-23	3000