

P-Channel Enhancement MOSFET

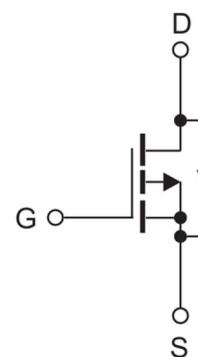
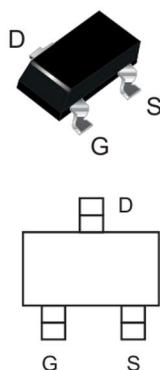
Features

- Low Thermal Resistance
- Low Gate Charge
- Halogen-Free & Lead-Free

Product Summary			
V _{DS}	R _{D(on)} (mΩ) Typ	I _D (A)	Q _g (Typ)
-20V	33 @ -4.5V	-5.6	7.2nC
	39 @ -2.5V	-4	

Application

- Load Switch for Portable Devices
- Voltage controlled small signal switch

SOT-23

P-channel MOSFET

Absolute Maximum Ratings (at T_A = 25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	-20	V
Gate-Source Voltage	V _{GS}	±10	V
Continuous Drain Current	T _A = 25°C	I _D	A
	T _A = 70°C	I _D	A
Peak Drain Current, Pulsed ¹⁾	I _{DM}	-22	A
Power Dissipation	P _D	1.2	W
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~150	°C

Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Ambient ²⁾	R _{θJA}	104	°C/W

Note:

1) Pulse width ≤300us, duty cycle ≤2%, limited by T_j max.

2) Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.

Characteristics at $T_J = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit
STATIC PARAMETERS					
Drain-Source Breakdown Voltage at $V_{GS}=0\text{V}$, $I_D=-250\mu\text{A}$	BV_{DSS}	-20			V
Drain-Source Leakage Current at $V_{DS}=-20\text{V}$, $V_{GS}=0\text{V}$	I_{DSS}			-1	μA
Gate Leakage Current at $V_{GS}=\pm 10\text{V}$, $V_{DS}=0\text{V}$	I_{GSS}			± 100	nA
Gate-Source Threshold Voltage at $V_{DS}=V_{GS}$, $I_D=-250\mu\text{A}$	$V_{GS(\text{th})}$	-0.4	-0.62	-1	V
Drain-Source On-State Resistance at $V_{GS} = -4.5\text{V}$, $I_D = -5.4\text{A}$ at $V_{GS} = -2.5\text{V}$, $I_D = -4\text{A}$ at $V_{GS} = -1.8\text{V}$, $I_D = -3\text{A}$	$R_{DS(\text{on})}$		33 39 49	42 55 75	$\text{m}\Omega$
DYNAMIC PARAMETERS					
Input Capacitance at $V_{GS}=0\text{V}$, $V_{DS}=-10\text{V}$, $f=1\text{MHz}$	C_{iss}		830		pF
Output Capacitance at $V_{GS}=0\text{V}$, $V_{DS}=-10\text{V}$, $f=1\text{MHz}$	C_{oss}		132		
Reverse Transfer Capacitance at $V_{GS}=0\text{V}$, $V_{DS}=-10\text{V}$, $f=1\text{MHz}$	C_{rss}		85		
Gate charge total at $V_{DS}=-10\text{V}$, $V_{GS}=-4.5\text{V}$, $I_D=-4\text{A}$	Q_g		7.2		nC
Gate to Source Charge at $V_{DS}=-10\text{V}$, $V_{GS}=-4.5\text{V}$, $I_D=-4\text{A}$	Q_{gs}		1.2		
Gate to Drain Charge at $V_{DS}=-10\text{V}$, $V_{GS}=-4.5\text{V}$, $I_D=-4\text{A}$	Q_{gd}		1.6		
Turn-On Delay Time at $V_{DD}=-10\text{V}$, $V_{GS}=-4.5\text{V}$, $R_{GEN}=3\Omega$, $R_L=2.5\Omega$	$t_{d(\text{on})}$		15		nS
Turn-On Rise Time at $V_{DD}=-10\text{V}$, $V_{GS}=-4.5\text{V}$, $R_{GEN}=3\Omega$, $R_L=2.5\Omega$	t_r		63		
Turn-Off Delay Time at $V_{DD}=-10\text{V}$, $V_{GS}=-4.5\text{V}$, $R_{GEN}=3\Omega$, $R_L=2.5\Omega$	$t_{d(\text{off})}$		21		
Turn-Off Fall Time at $V_{DD}=-10\text{V}$, $V_{GS}=-4.5\text{V}$, $R_{GEN}=3\Omega$, $R_L=2.5\Omega$	t_f		12		
Body-Diode PARAMETERS					
Drain-Source Diode Forward Voltage at $I_S=-1\text{A}$, $V_{GS}=0\text{V}$	V_{SD}		-0.8	-1.2	V
Maximum Body-Diode Continuous Current	I_S			-5.6	A

Electrical Characteristics Curves

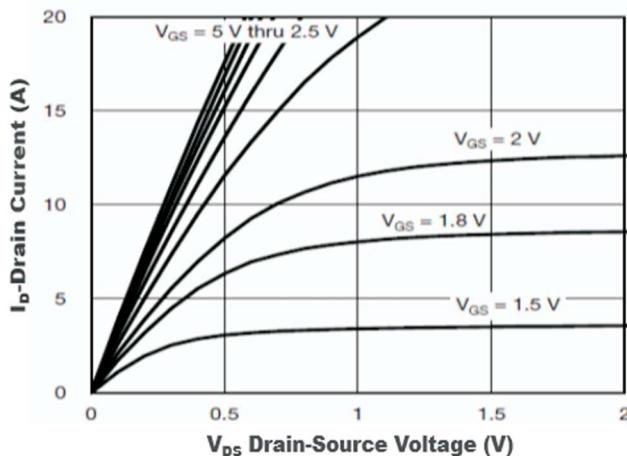


Figure1. Output Characteristics

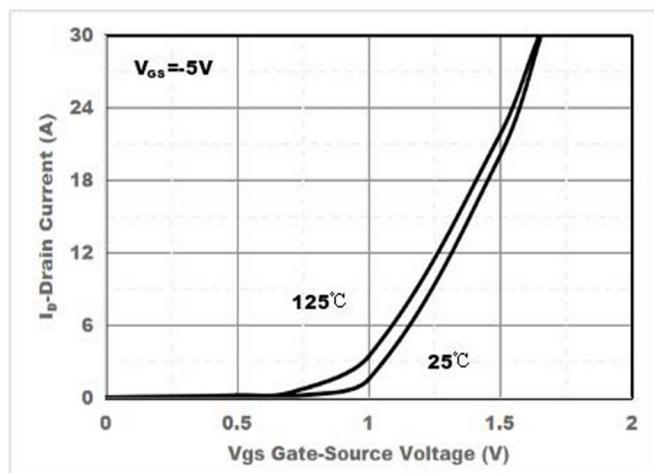


Figure2. Transfer Characteristics

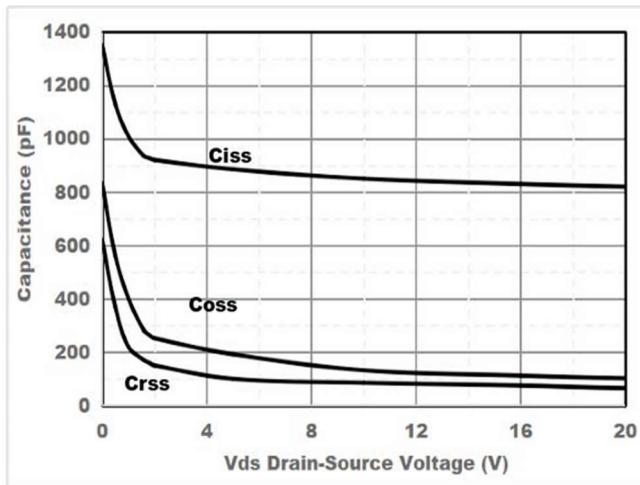


Figure3. Capacitance Characteristics

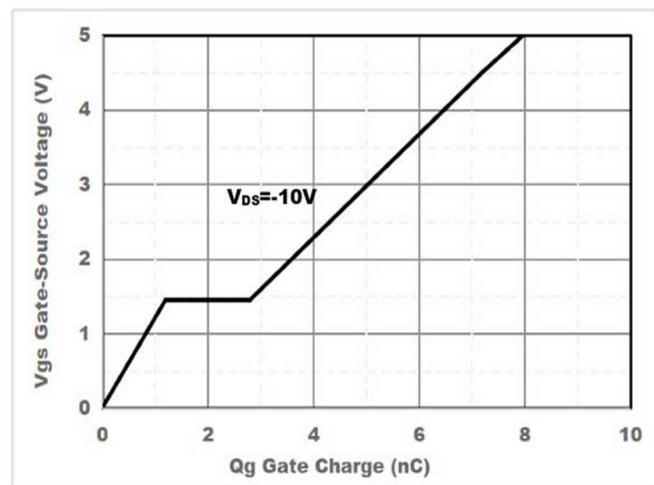


Figure4. Gate Charge

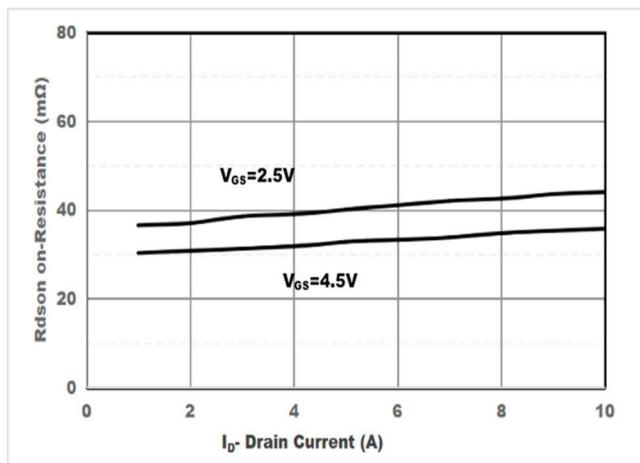


Figure5. Drain-Source on Resistance

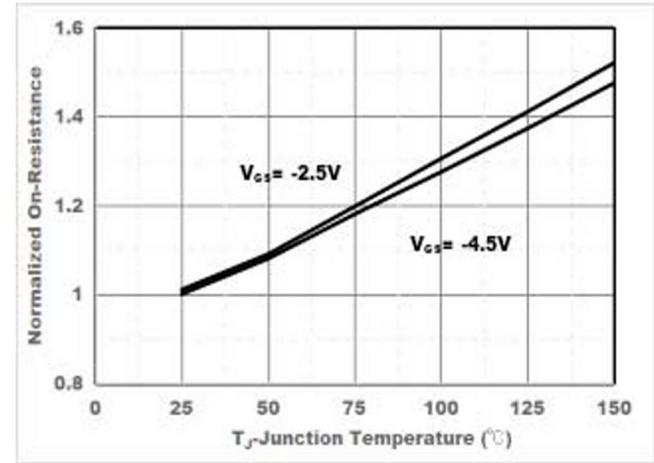


Figure6. Drain-Source on Resistance

Electrical Characteristics Curves

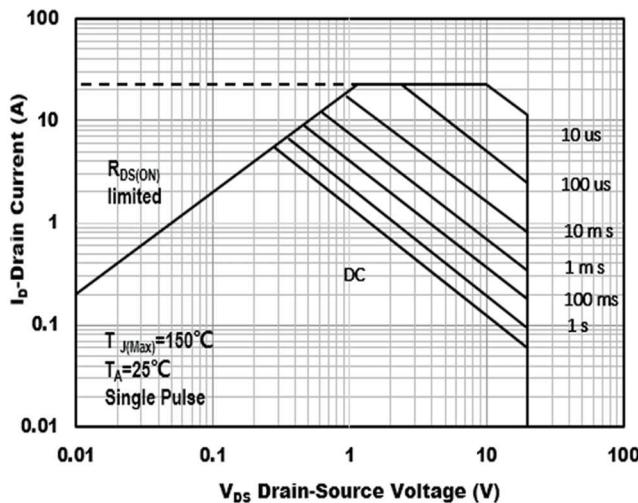


Figure 7. Safe Operation Area

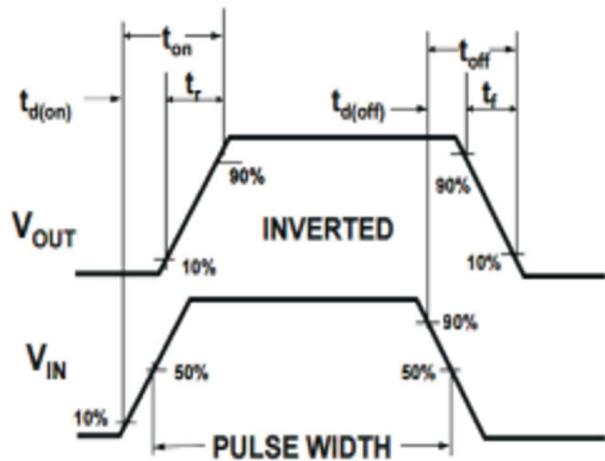
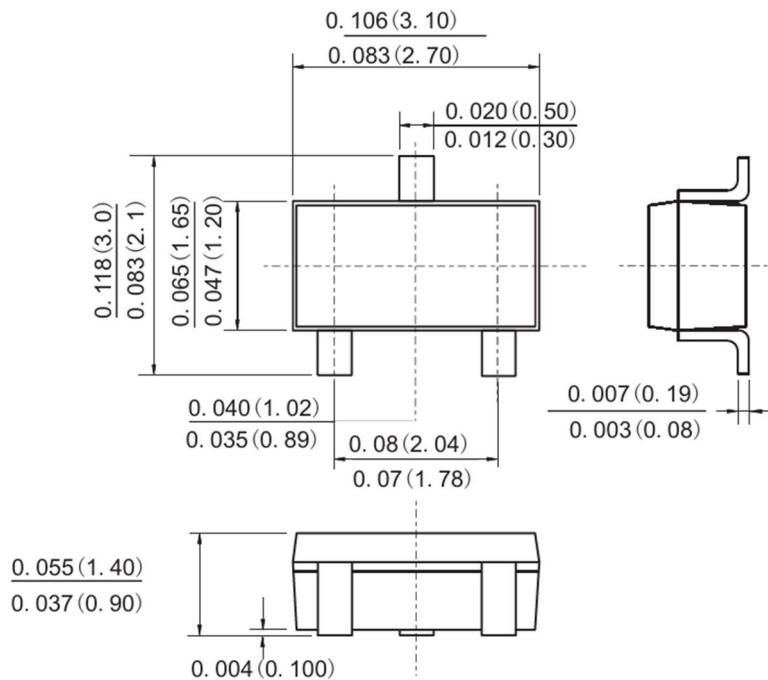


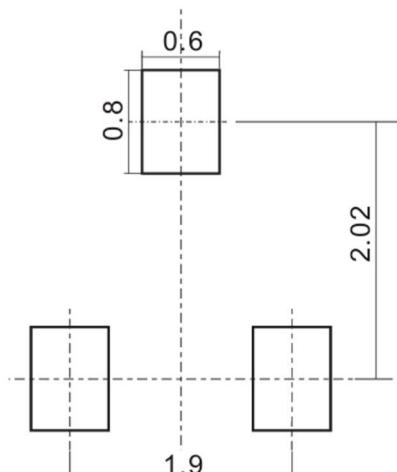
Figure 8. Switching wave

Order Information

Part Number	Package	Marking	Quantity
Sh2305B	SOT-23	S5B or A5	3000

Package Outline Dimensions (Units: mm) SOT-23

Dimensions in inches and (millimetres)

Suggested Pad Layout

Dimensions in millimetres