

N-Channel MOSFET

Features

- Trench Power MV MOSFET Technology
- Voltage Controlled Small Signal Switch
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate

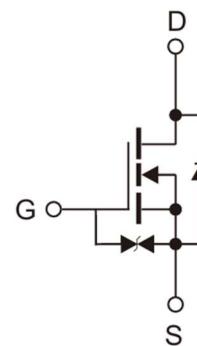
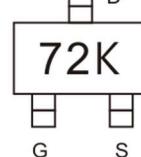
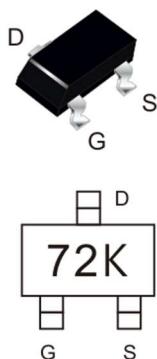
Product Summary		
V _{DS}	R _{Ds(on)} (Ω) Typ	I _D (mA)
60V	1.1@ 4.5V 0.2A	340
	0.9@ 10V 0.5A	

Application

- Load Switch for Portable Devices
- Solid-state relays

Marking information

SOT-523



N-channel MOSFET

Absolute Maximum Ratings (at T_A = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage (T _A =25 °C)	V _{GS}	±20	V
Continuous drain current (T _A =25 °C)	I _D	0.34	A
Peak Drain Current, Pulsed ¹⁾	I _{DM}	0.8	A
Power Dissipation	P _D	0.15	W
Operating Junction	T _J	-55~150	°C
Storage Temperature Range	T _{stg}	-55~150	°C

Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Ambient	R _{θJA}	425	°C/W

Characteristics at $T_J = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit
STATIC PARAMETERS					
Drain-Source Breakdown Voltage at $V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	BV_{DSS}	60			V
Drain-Source Leakage Current at $V_{DS}=48\text{V}$, $V_{GS}=0\text{V}$	I_{DSS}			1	μA
Gate Leakage Current at $V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$	I_{GSS}			± 10	μA
Gate-Source Threshold Voltage at $V_{DS}=V_{GS}$, $I_D=1\text{mA}$	$V_{GS(\text{th})}$	1	1.3	2.5	V
Drain-Source On-State Resistance at $V_{GS}=10\text{V}$, $I_D=500\text{mA}$ at $V_{GS}=4.5\text{V}$, $I_D=200\text{mA}$	$R_{DS(\text{on})}$		0.9 1.1	2.7 3.0	Ω
DYNAMIC PARAMETERS					
Input Capacitance at $V_{DS}=10\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	C_{iss}			40	pF
Output Capacitance at $V_{DS}=10\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	C_{oss}			30	pF
Reverse Transfer Capacitance at $V_{DS}=10\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	C_{rss}			10	pF
Gate charge total at $V_{DS}=30\text{V}$, $V_{GS}=10\text{V}$, $I_D=0.3\text{A}$	Q_g		1.65		nc
Gate to Source Charge at $V_{DS}=30\text{V}$, $V_{GS}=10\text{V}$, $I_D=0.3\text{A}$	Q_{gs}		10.4		nc
Gate to Drain Charge at $V_{DS}=30\text{V}$, $V_{GS}=10\text{V}$, $I_D=0.3\text{A}$	Q_{gd}		0.24		nc
Turn-On Delay Time at $V_{DD}=50\text{V}$, $R_L=250\Omega$, $R_{GEN}=50\Omega$, $V_{GS}=10\text{V}$	$t_{d(on)}$			10	nS
Turn-Off Delay Time at $V_{DD}=50\text{V}$, $R_L=250\Omega$, $R_{GEN}=50\Omega$, $V_{GS}=10\text{V}$	$t_{d(off)}$			15	nS
Reverse Recovery Time at $V_R=25\text{V}$, $V_{GS}=0\text{V}$ $I_S=0.3\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$	trr		30		nS
Body-Diode PARAMETERS					
Drain-Source Diode Forward Voltage at $I_S=0.3\text{A}$, $V_{GS}=0\text{V}$	V_{SD}			1.5	V
Maximum Body-Diode Continuous Current	I_S			200	mA

Note:

1) Pulse Test: Pulse width $\leq 300\text{us}$, Duty cycle $\leq 2\%$.

Electrical Characteristics Curves

Figure 1. Output Characteristics

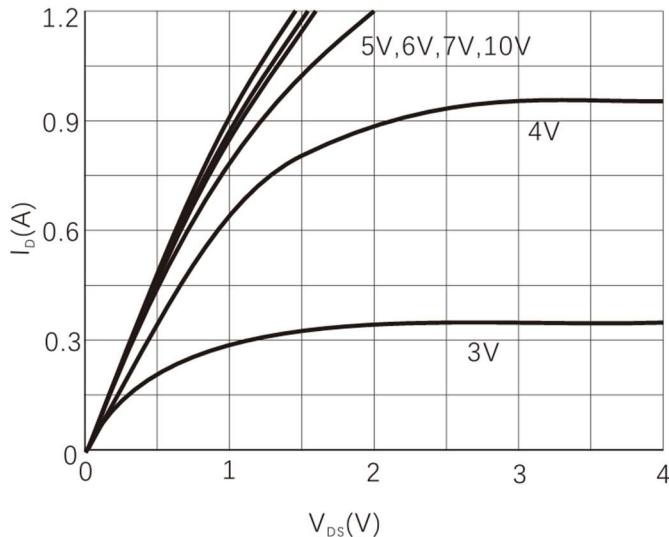


Figure 3. On-Resistance vs. Drain Current

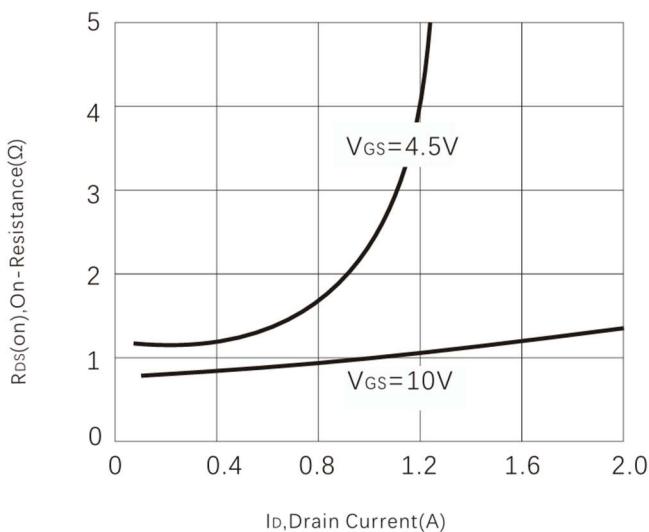


Figure 5. Gate charge

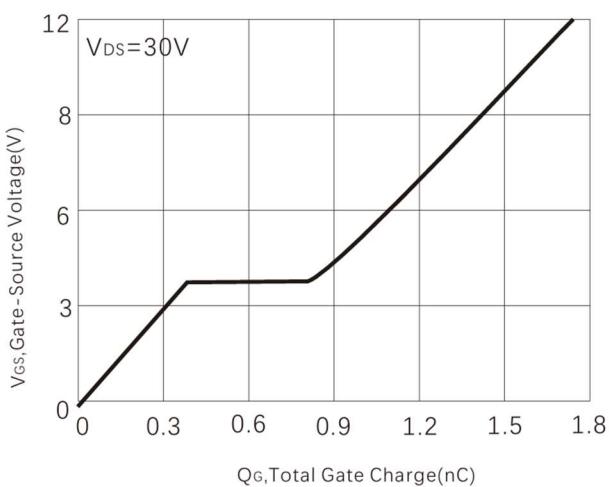


Figure 2. Transfer Characteristics

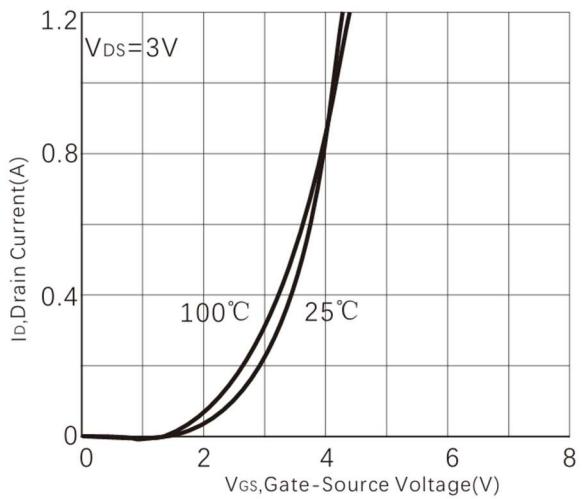


Figure 4. Capacitance

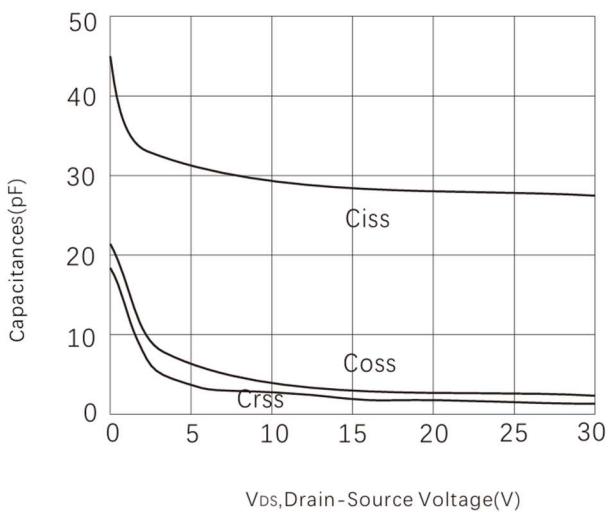


Figure 6. Normalized RDS(ON) vs Junction Temperature

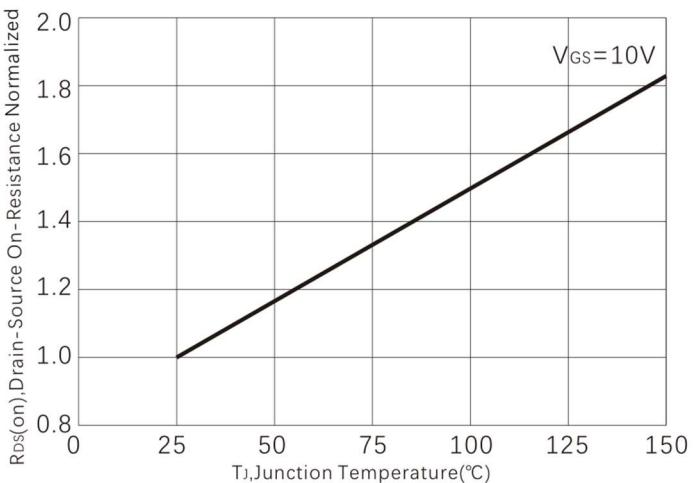
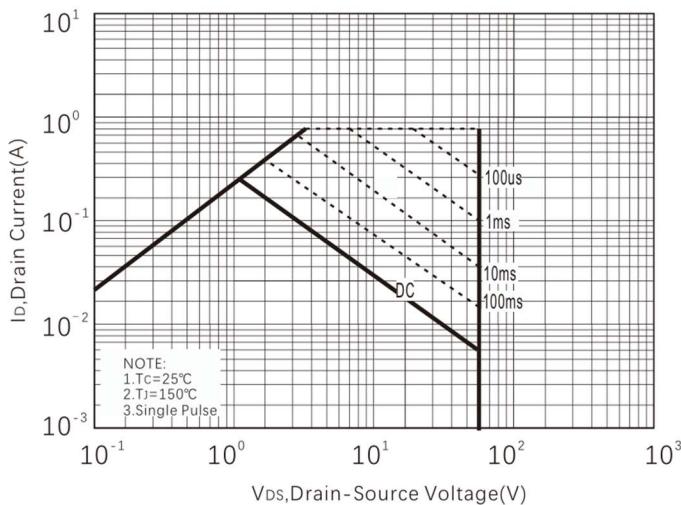
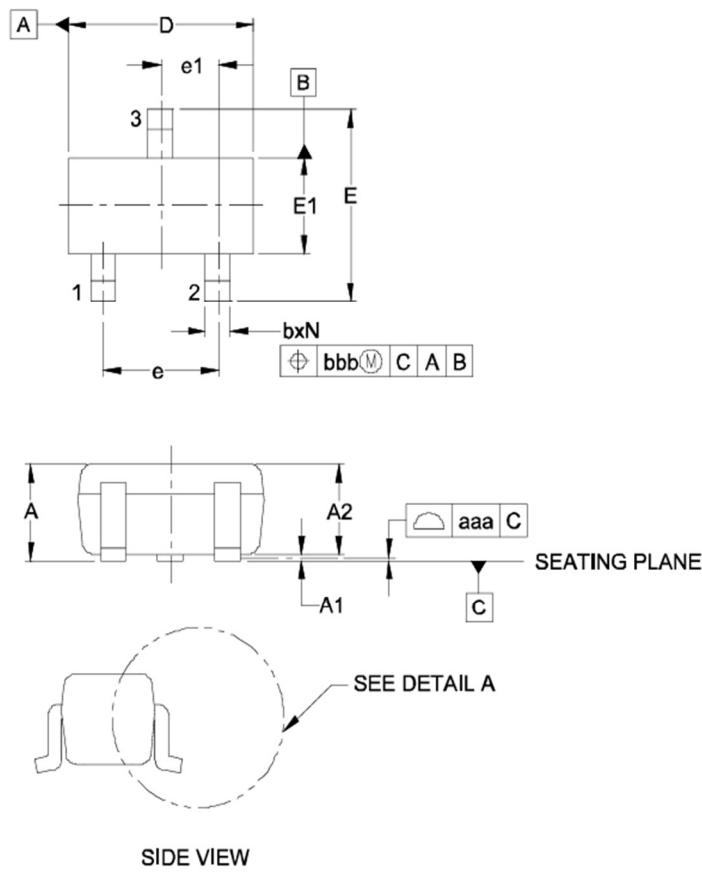
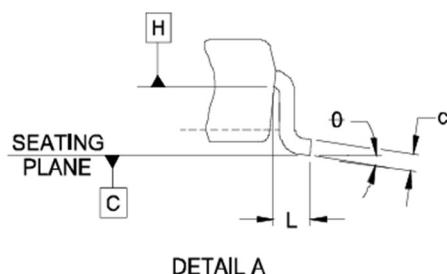
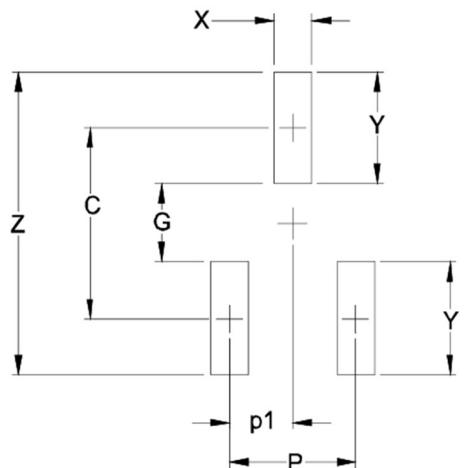


Figure 7. Safe operating area

**Package Outline Dimensions (Units: mm) SOT-523**

DIM	INCHES			MILLIMETERS		
	MIN	NOM	MAX	MIN	NOM	MAX
A	.023	-	.035	0.60	-	0.90
A1	.000	-	.004	0.00	-	0.10
A2	.023	.030	.031	0.60	0.75	0.80
b	.005	-	.012	0.15	-	0.30
c	.003	-	.008	0.10	-	0.20
D	.059	.063	.067	1.50	1.60	1.70
E	.057	.063	.069	1.45	1.60	1.75
E1	.029	.031	.033	0.75	0.80	0.85
e	.039 BSC			1.00 BSC		
e1	.020 BSC			0.50 BSC		
L	(.009)			(0.22)		
N	3			3		
θ	0°	-	8°	0°	-	8°
aaa	.004			0.10		
bbb	.008			0.20		



Suggested Pad Layout

SYM	DIMENSIONS	
	INCHES	MILLIME-TERS
C	(.055)	(1.40)
P	.039	1.00
P1	.020	0.50
G	.024	0.60
X	.016	0.40
Y	.031	0.80
Z	.087	2.20

Order Information

Part Number	Package	Quantity
Sh2N7002KT	SOT-523	3000