

## N-Channel Enhancement MOSFET

### Features

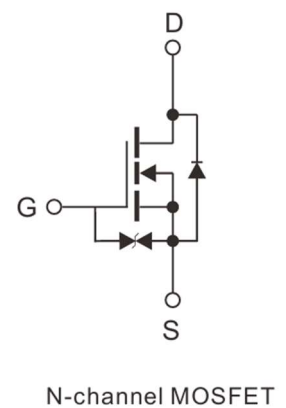
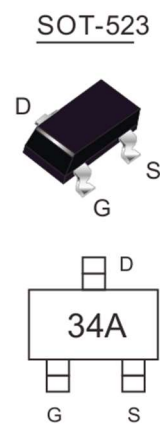
- Trench Power LV MOSFET technology
- Voltage Controlled Small Signal Switch
- High Power and current handling capability
- ESD Protected Gate

Product Summary		
$V_{DS}$	$R_{DS(on)}$ (m $\Omega$ ) Typ	$I_D$ (A)
20V	220@ 4.5V, 0.5A	0.5
	290@ 2.5V, 0.4A	

### Application

- Load Switch for Portable Devices
- Solid-state relays

### Marking information



### Absolute Maximum Ratings (at $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Continuous drain current ( $T_A=25^\circ\text{C}$ )	$I_D$	0.5	A
Continuous drain current ( $T_A=70^\circ\text{C}$ )	$I_D$	0.4	A
Pulsed Drain Current <sup>1)</sup>	$I_{DM}$	3.3	A
Power Dissipation	$P_D$	0.18	W
Operating Junction	$T_J$	-55~150	$^\circ\text{C}$
Storage Temperature Range	$T_{stg}$	-55~150	$^\circ\text{C}$

### Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	694	$^\circ\text{C/W}$

Note: 1. Pulse test: 300 $\mu\text{s}$  pulse width, 1% duty cycle.

**Characteristics at T<sub>J</sub> = 25°C unless otherwise specified**

Parameter	Symbol	Min.	Typ.	Max.	Unit
<b>STATIC PARAMETERS</b>					
Drain-Source Breakdown Voltage at V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	BV <sub>DSS</sub>	20			V
Drain-Source Leakage Current at V <sub>DS</sub> =20V, V <sub>GS</sub> =0V	I <sub>DSS</sub>			1	μA
Gate Leakage Current at V <sub>GS</sub> =±10V, V <sub>DS</sub> =0V	I <sub>GSS</sub>			±10	μA
Gate-Source Threshold Voltage at V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	V <sub>GS(th)</sub>	0.35	0.75	1.1	V
Drain-Source On-State Resistance at V <sub>GS</sub> =4.5V, I <sub>D</sub> =0.5A at V <sub>GS</sub> =2.5V, I <sub>D</sub> =0.4A at V <sub>GS</sub> =1.8V, I <sub>D</sub> =0.2A	R <sub>DS(on)</sub>		220 290 420	300 400 700	mΩ
<b>DYNAMIC PARAMETERS</b>					
Input Capacitance at V <sub>DS</sub> =10V, V <sub>GS</sub> =0V, f=1MHz	C <sub>iss</sub>		56		pF
Output Capacitance at V <sub>DS</sub> =10V, V <sub>GS</sub> =0V, f=1MHz	C <sub>oss</sub>		20		pF
Reverse Transfer Capacitance at V <sub>DS</sub> =10V, V <sub>GS</sub> =0V, f=1MHz	C <sub>rss</sub>		2.5		pF
Gate charge total at V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =0.5A	Q <sub>g</sub>		1.0		nc
Gate to Source Charge at V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =0.5A	Q <sub>gs</sub>		0.28		nc
Gate to Drain Charge at V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =0.5A	Q <sub>gd</sub>		0.22		nc
Turn-On Delay Time at V <sub>DD</sub> =10V, I <sub>D</sub> =0.5A, R <sub>GEN</sub> =10Ω, V <sub>GS</sub> =4.5V	t <sub>d(on)</sub>		2		ns
Turn-On Rise Time at V <sub>DD</sub> =10V, I <sub>D</sub> =0.5A, R <sub>GEN</sub> =10Ω, V <sub>GS</sub> =4.5V	t <sub>r</sub>		18.8		ns
Turn-Off Delay Time at V <sub>DD</sub> =10V, I <sub>D</sub> =0.5A, R <sub>GEN</sub> =10Ω, V <sub>GS</sub> =4.5V	t <sub>d(off)</sub>		10		ns
Turn-On Fall Time at V <sub>DD</sub> =10V, I <sub>D</sub> =0.5A, R <sub>GEN</sub> =10Ω, V <sub>GS</sub> =4.5V	t <sub>f</sub>		23		ns
Reverse Recovery Time I <sub>F</sub> =0.5A, di/dt=20A/μs	t <sub>rr</sub>		14.4		ns
Reverse Recovery Charge I <sub>F</sub> =0.5A, di/dt=20A/μs	Q <sub>rr</sub>		0.4		nc
<b>Body-Diode PARAMETERS</b>					
Drain-Source Diode Forward Voltage at I <sub>S</sub> =0.5A, V <sub>GS</sub> =0V	V <sub>SD</sub>			1.2	V
Maximum Body-Diode Continuous Current	I <sub>S</sub>			0.5	A

**Electrical Characteristics Curves**

Figure1. Output Characteristics

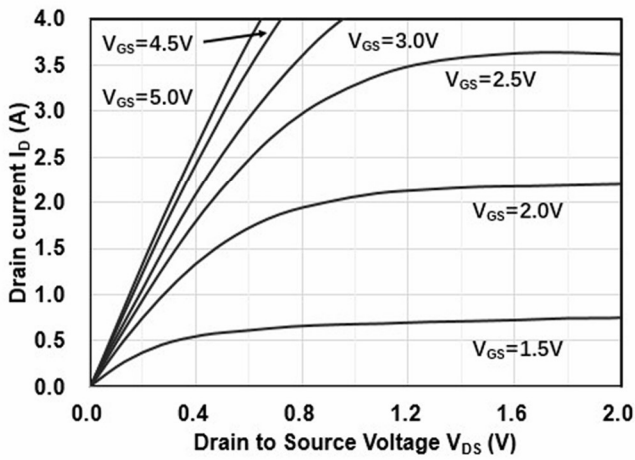


Figure3. Capacitance Characteristics

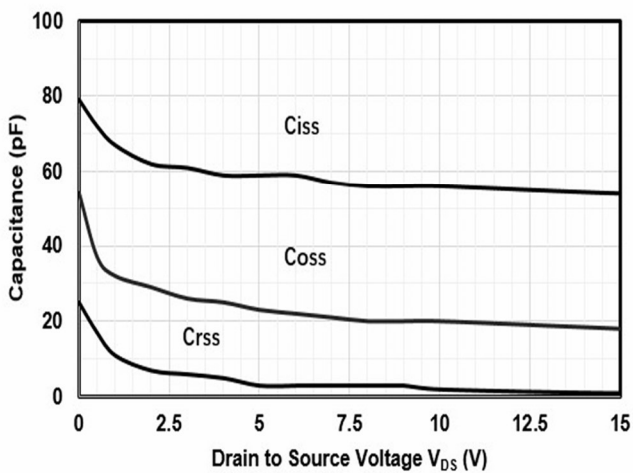


Figure5. Drain-Source on Resistance

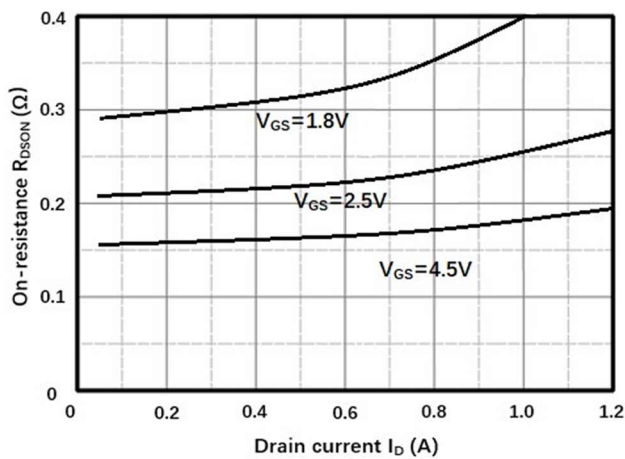


Figure2. Transfer Characteristics

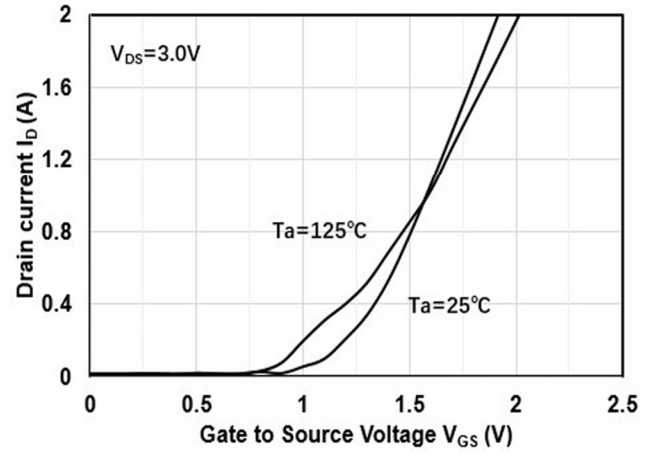


Figure4. Gate Charge

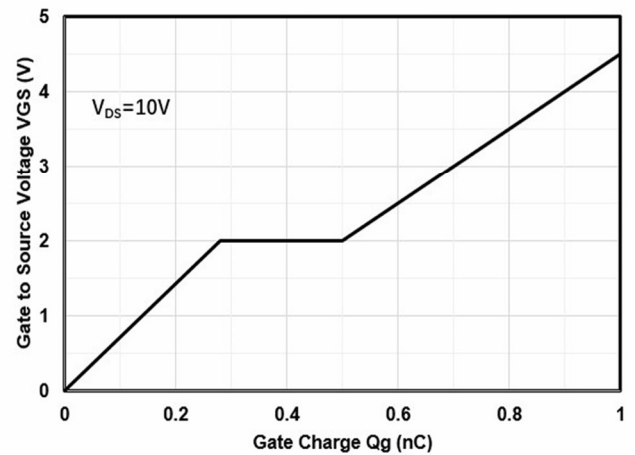


Figure6. Drain-Source on Resistance

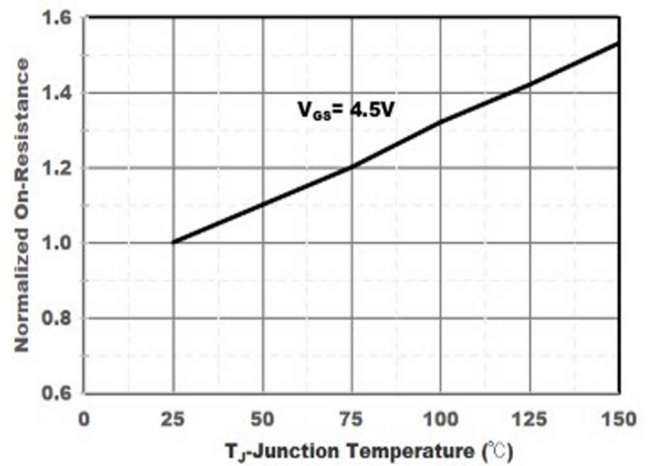
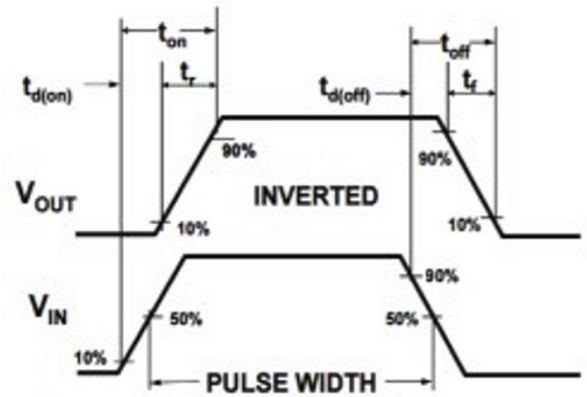
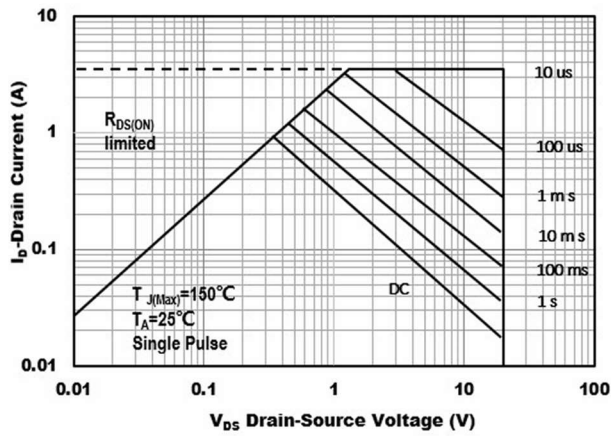
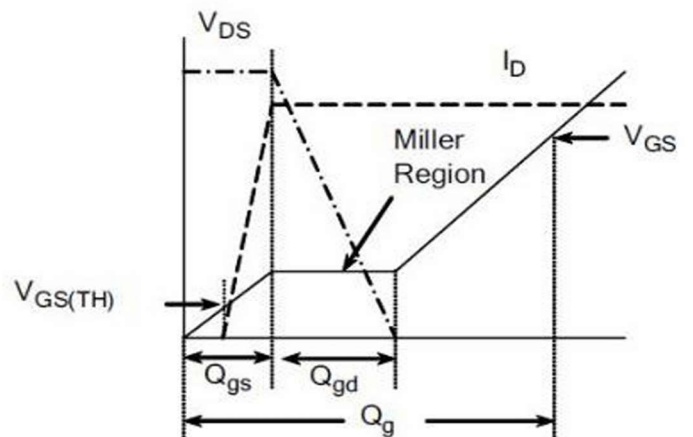
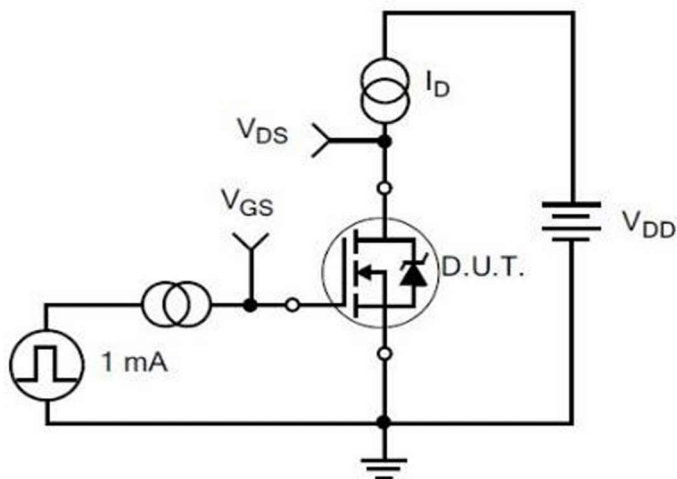


Figure7. Safe Operation Area

Figure8. Switching wave

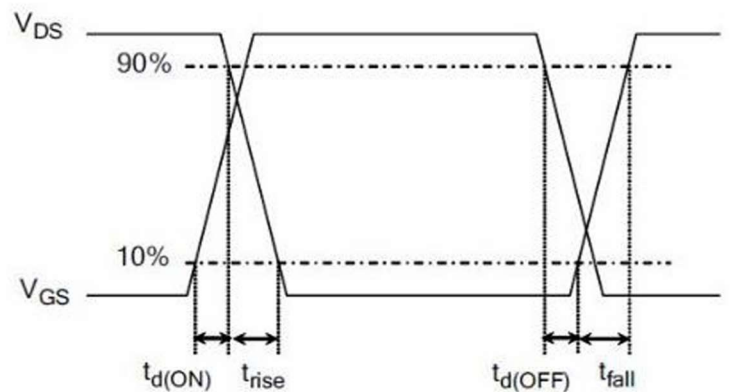
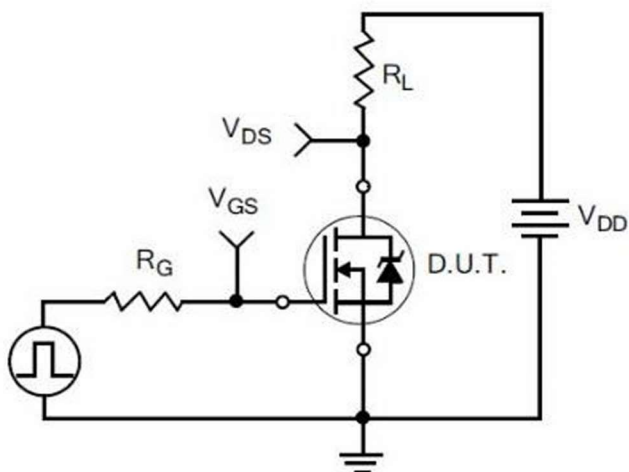


## Test Circuits & Waveforms



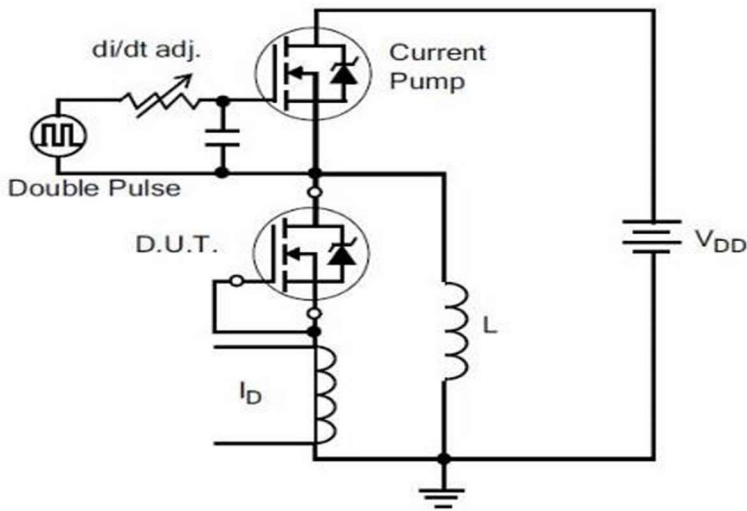
1) Gate Charge Test Circuit

2) . Gate Charge Waveform

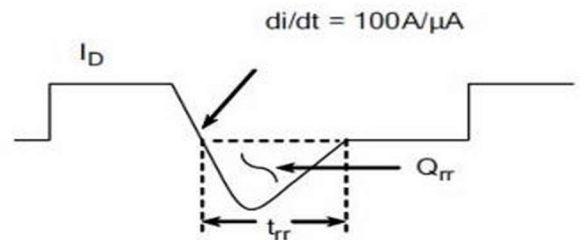


3) Resistive Switching Test Circuit

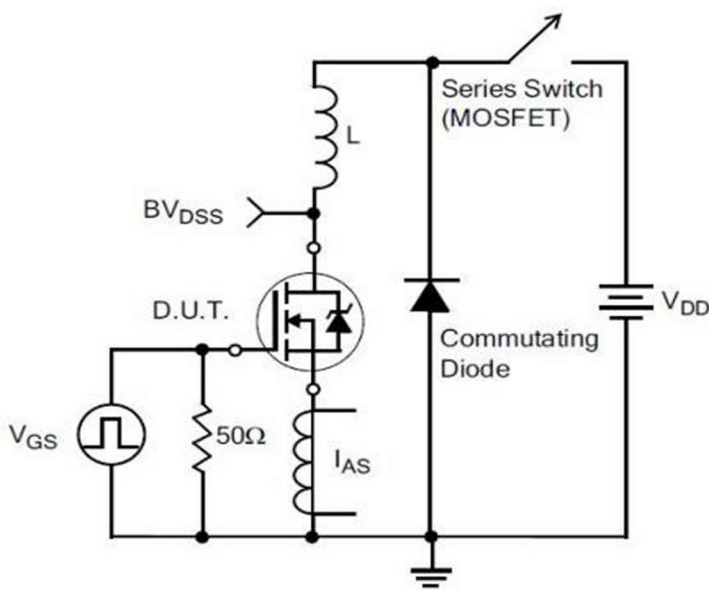
4) Resistive Switching Waveforms



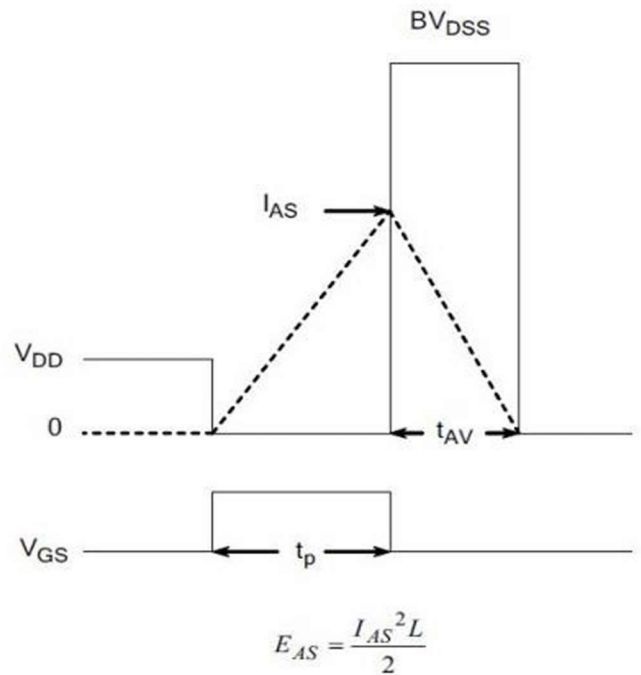
5) Diode Reverse Recovery Test Circuit



6) Diode Reverse Recovery Waveform



7) . Unclamped Inductive Switching Test Circuit

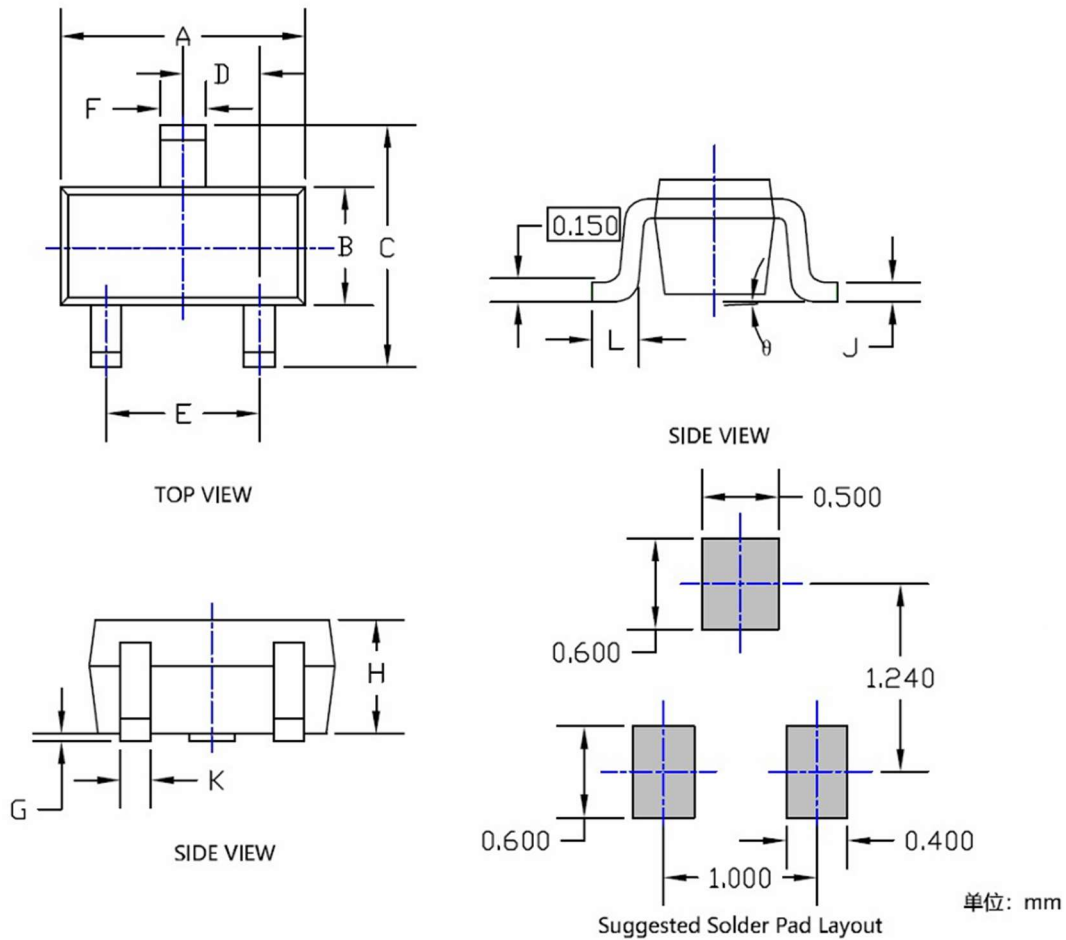


8) Unclamped Inductive Switching Waveforms

Order Information

Part Number	Package	Quantity
Sh3134KAE	SOT-523	3000

## Package Outline Dimensions (Units: mm) SOT-523



DIMENSIONS						
SYMBOL	INCHES			Millimeter		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	0.059	0.063	0.067	1.500	1.600	1.700
B	0.030	0.031	0.033	0.750	0.800	0.850
C	0.057	0.063	0.069	1.450	1.600	1.750
D	0.020TYP			0.500TYP		
E	0.035	0.039	0.043	0.900	1.000	1.100
F	0.010	0.014	0.018	0.250	0.350	0.450
G	0.000	---	0.004	0.000	---	0.100
H	0.024	0.028	0.031	0.600	0.700	0.800
J	0.004	---	0.008	0.100	---	0.200
K	0.006	0.010	0.014	0.150	0.250	0.350
L	0.010	---	0.018	0.260	---	0.460
θ	0°	---	8°	0°	---	8°

**NOTE:**

1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
2. TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.
3. THE PAD LAYOUT IS FOR REFERENCE PURPOSES ONLY.