

P-Channel Enhancement MOSFET

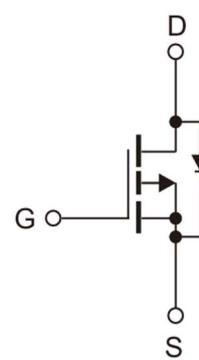
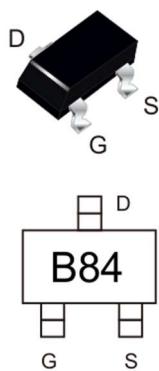
Features

- Trench Power LV MOSFET technology
- High speed switching
- Halogen-Free & Lead-Free

Product Summary		
V _{DS}	R _{DSS} (₀) (mΩ) Max	I _D (A)
-60V	8.0 @ -10V	-0.17
	10.0 @ -4.5V	

Application

- Load switch
- Battery protection
- Power management

Marking informationSOT-23**Absolute Maximum Ratings (at T_A = 25°C unless otherwise noted)**

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	-60	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current	T _A = 25°C	I _D	A
	T _A = 70°C	I _D	A
Pulse Drain Current ¹⁾	I _{DM}	-0.68	A
Power Dissipation T _A = 25°C	P _D	0.225	W
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

Thermal Characteristics

Parameter	Symbol	Max.	Unit
Thermal Resistance from Junction to Ambient ²⁾	R _{θJA}	556	°C/W

Note:

1) Pulse width ≤300us, duty cycle ≤2%, limited by T_j max.

2) Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.

Characteristics at $T_J = 25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit
STATIC PARAMETERS					
Drain-Source Breakdown Voltage at $V_{GS}=0\text{V}$, $I_D=-250\mu\text{A}$	BV_{DSS}	-60			V
Drain-Source Leakage Current at $V_{DS}=-60\text{V}$, $V_{GS}=0\text{V}$	I_{DSS}			-1	μA
Gate Leakage Current at $V_{GS}=\pm20\text{V}$, $V_{DS}=0\text{V}$	I_{GSS}			±100	nA
Gate-Source Threshold Voltage at $V_{DS}=V_{GS}$, $I_D=-250\mu\text{A}$	$V_{GS(\text{th})}$	-0.9	-1.4	-2.0	V
Drain-Source On-State Resistance at $V_{GS} = -10\text{V}$, $I_D = -0.15\text{A}$ at $V_{GS} = -4.5\text{V}$, $I_D = -0.15\text{A}$	$R_{DS(\text{on})}$		3.3 3.5	8 10	Ω
DYNAMIC PARAMETERS					
Input Capacitance at $V_{GS}=0\text{V}$, $V_{DS}=-30\text{V}$, $f=1\text{MHz}$	C_{iss}		30		pF
Output Capacitance at $V_{GS}=0\text{V}$, $V_{DS}=-30\text{V}$, $f=1\text{MHz}$	C_{oss}		10		
Reverse Transfer Capacitance at $V_{GS}=0\text{V}$, $V_{DS}=-30\text{V}$, $f=1\text{MHz}$	C_{rss}		5		
Turn-On Delay Time at $V_{DD}=-30\text{V}$, $V_{GS}=-4.5\text{V}$, $R_{GEN}=2.5\Omega$, $I_D=-0.15\text{A}$	$t_{d(\text{on})}$		2.5		nS
Turn-On Rise Time at $V_{DD}=-30\text{V}$, $V_{GS}=-4.5\text{V}$, $R_{GEN}=2.5\Omega$, $I_D=-0.15\text{A}$	t_r		1		
Turn-Off Delay Time at $V_{DD}=-30\text{V}$, $V_{GS}=-4.5\text{V}$, $R_{GEN}=2.5\Omega$, $I_D=-0.15\text{A}$	$t_{d(\text{off})}$		16		
Turn-Off Fall Time at $V_{DD}=-30\text{V}$, $V_{GS}=-4.5\text{V}$, $R_{GEN}=2.5\Omega$, $I_D=-0.15\text{A}$	t_f		8		
Body-Diode PARAMETERS					
Drain-Source Diode Forward Voltage at $I_S=-0.17\text{A}$, $V_{GS}=0\text{V}$	V_{SD}			-1.2	V
Maximum Body-Diode Continuous Current	I_S			-0.17	A

Electrical Characteristics Curves

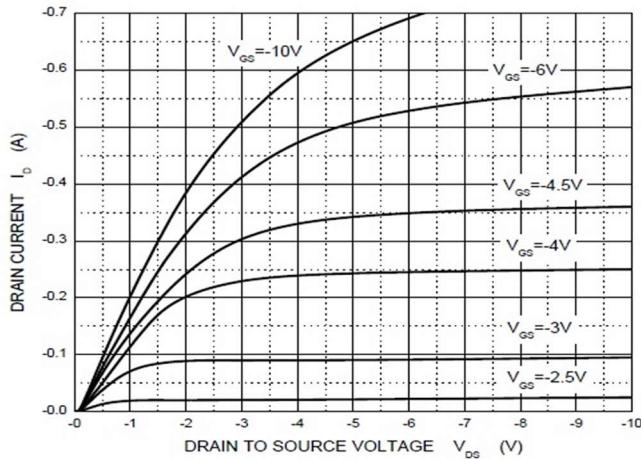


Figure1. Output Characteristics

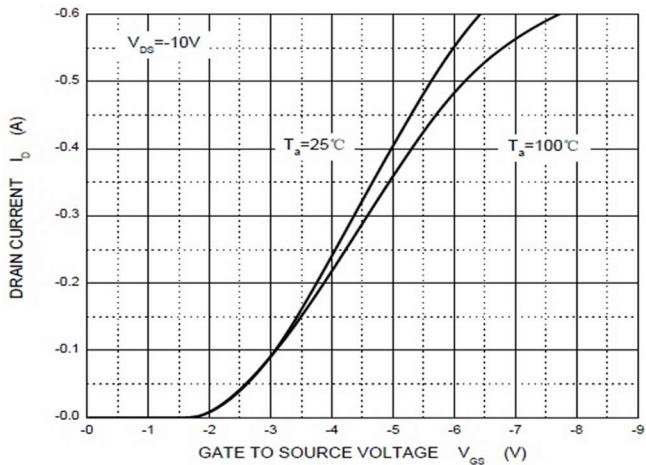


Figure2. Transfer Characteristics

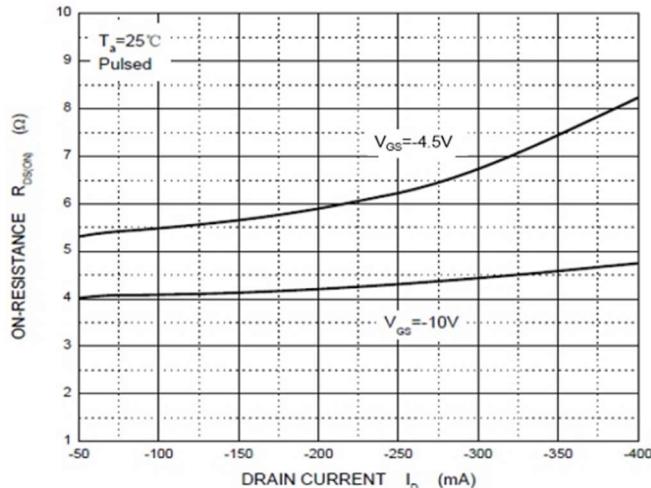


Figure3. Drain-Source on Resistance

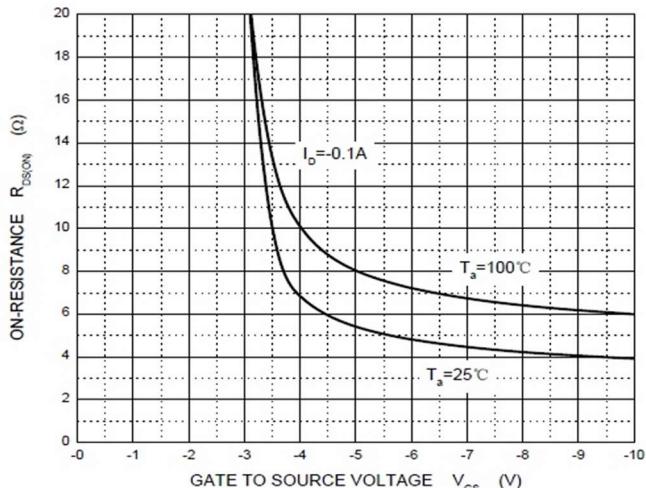


Figure4. Drain-Source on Resis tance

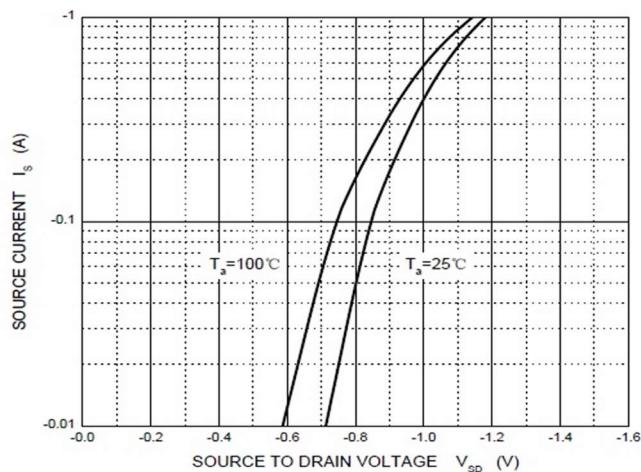


Figure5. Diode Forward Voltage vs. current

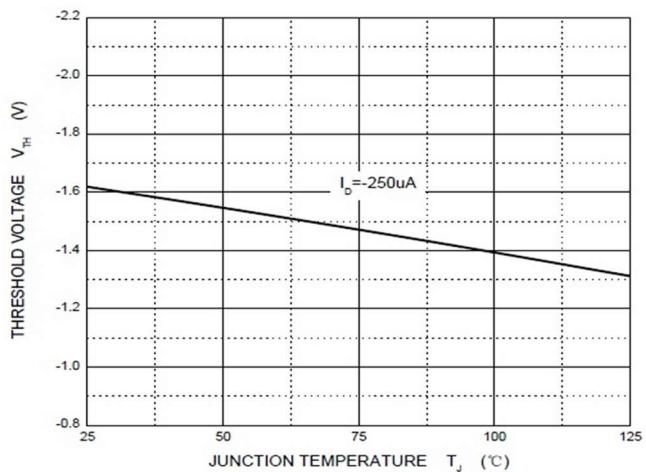
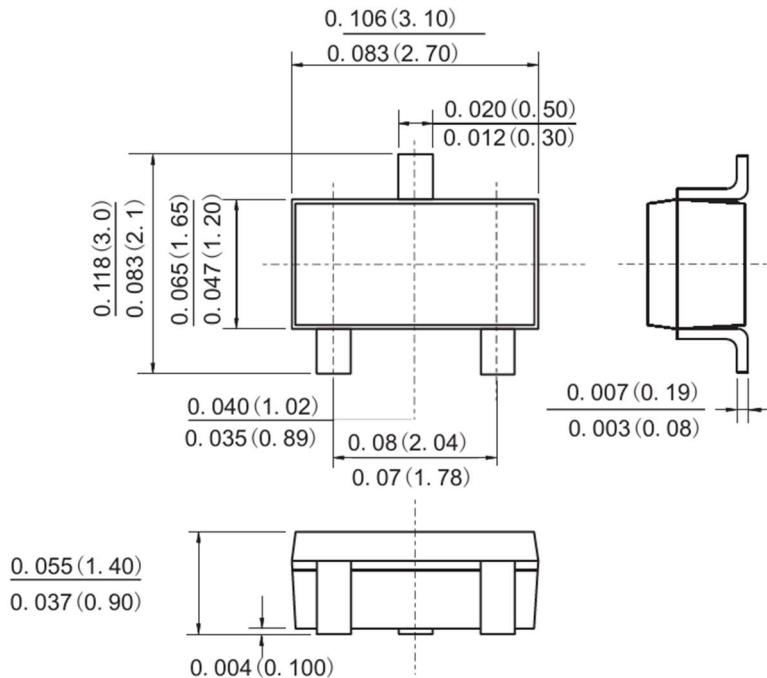


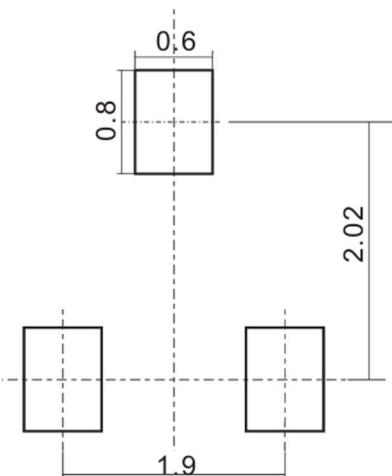
Figure6. Gate Threshold vs. Junction Temperature

Order Information

Part Number	Package	Quantity
ShBSS84	SOT-23	3000

Package Outline Dimensions (Units: mm) SOT-23

Dimensions in inches and (millimetres)

Suggested Pad Layout

Dimensions in millimetres